Is Now Part of

ON Semiconductor®

To learn more about ON Semiconductor, please visit our website at
www.onsemi.com

FAIRCHILD SEMICONDUCTOR®

November 2013

FGH60N60SMD
600 V, 60 A Field Stop IGBT

Features

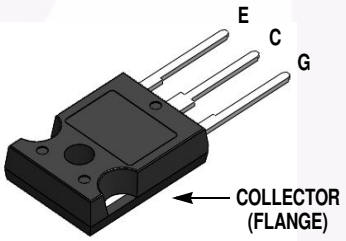
- Maximum Junction Temperature: $T_J = 175^oC$
- Positive Temperaure Co-efficient for easy Parallel Operating
- High Current Capability
- Low Saturation Voltage: $V_{CE(sat)}$ = 1.9 V(Typ.) @ I_C = 60 A
- High Input Impedance
- Fast Switching: E_{OFF} = 7.5 uJ/A
- Tightened Parameter Distribution
- RoHS Compliant

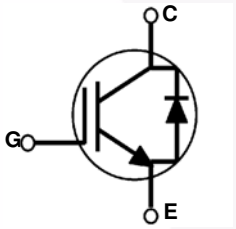
Applications

- Solar Inverter, UPS, Welder, PFC, Telecom, ESS

General Description

Using novel field stop IGBT technology, Fairchild's new series of field stop 2nd generation IGBTs offer the optimum performance for solar inverter, UPS, welder, telecom, ESS and PFC applications where low conduction and switching losses are essential.





Absolute Maximum Ratings

Symbol	Description		Ratings	Unit
V_{CES}	Collector to Emitter Voltage		600	V
V_{GES}	Gate to Emitter Voltage		± 20	V
	Transient Gate-to-Emitter Voltage		± 30	V
I_C	Collector Current	@ $T_C = 25^oC$	120	A
	Collector Current	@ $T_C = 100^oC$	60	A
$I_{CM\ (1)}$	Pulsed Collector Current		180	A
I_F	Diode Forward Current	@ $T_C = 25^oC$	60	A
	Diode Forward Current	@ $T_C = 100^oC$	30	A
$I_{FM\ (1)}$	Pulsed Diode Maximum Forward Current		180	A
P_D	Maximum Power Dissipation	@ $T_C = 25^oC$	600	W
	Maximum Power Dissipation	@ $T_C = 100^oC$	300	W
T_J	Operating Junction Temperature		-55 to +175	^oC
T_{stg}	Storage Temperature Range		-55 to +175	^oC
T_L	Maximum Lead Temp. for soldering Purposes, 1/8" from case for 5 seconds		300	^oC

Notes:
1: Repetitive rating: Pulse width limited by max. junction temperature

Thermal Characteristics

Symbol	Parameter	Typ.	Max.	Unit
$R_{\theta JC}$(IGBT)	Thermal Resistance, Junction to Case	-	0.25	°C/W
$R_{\theta JC}$(Diode)	Thermal Resistance, Junction to Case	-	1.1	°C/W
$R_{\theta JA}$	Thermal Resistance, Junction to Ambient	-	40	°C/W

Package Marking and Ordering Information

Part Number	Top Mark	Package	Packing Method	Reel Size	Tape Width	Quantity
FGH60N60SMD	FGH60N60SMD	TO-247	Tube	N/A	N/A	30

Electrical Characteristics of the IGBT T_C = 25°C unless otherwise noted

Symbol	Parameter	Test Conditions	Min.	Typ.	Max.	Unit
Off Characteristics						
BV_{CES}	Collector to Emitter Breakdown Voltage	V_{GE} = 0 V, I_C = 250 μA	600	-	-	V
$\frac{\Delta BV_{CES}}{\Delta T_J}$	Temperature Coefficient of Breakdown Voltage	V_{GE} = 0 V, I_C = 250 μA	-	0.6	-	V/°C
I_{CES}	Collector Cut-Off Current	V_{CE} = V_{CES}, V_{GE} = 0 V	-	-	250	μA
I_{GES}	G-E Leakage Current	V_{GE} = V_{GES}, V_{CE} = 0 V	-	-	±400	nA
On Characteristics						
$V_{GE(th)}$	G-E Threshold Voltage	I_C = 250 μA, V_{CE} = V_{GE}	3.5	4.5	6.0	V
$V_{CE(sat)}$	Collector to Emitter Saturation Voltage	I_C = 60 A, V_{GE} = 15 V	-	1.9	2.5	V
		I_C = 60 A, V_{GE} = 15 V, T_C = 175°C	-	2.1	-	V
Dynamic Characteristics						
C_{ies}	Input Capacitance	V_{CE} = 30 V, V_{GE} = 0 V, f = 1 MHz	-	2915	-	pF
C_{oes}	Output Capacitance		-	270	-	pF
C_{res}	Reverse Transfer Capacitance		-	85	-	pF
Switching Characteristics						
$t_{d(on)}$	Turn-On Delay Time	V_{CC} = 400 V, I_C = 60 A, R_G = 3 Ω, V_{GE} = 15 V, Inductive Load, T_C = 25°C	-	18	27	ns
t_r	Rise Time		-	47	70	ns
$t_{d(off)}$	Turn-Off Delay Time		-	104	146	ns
t_f	Fall Time		-	50	68	ns
E_{on}	Turn-On Switching Loss		-	1.26	1.94	mJ
E_{off}	Turn-Off Switching Loss		-	0.45	0.6	mJ
E_{ts}	Total Switching Loss		-	1.71	2.54	mJ
$t_{d(on)}$	Turn-On Delay Time	V_{CC} = 400 V, I_C = 60 A, R_G = 3 Ω, V_{GE} = 15 V, Inductive Load, T_C = 175°C	-	18	-	ns
t_r	Rise Time		-	41	-	ns
$t_{d(off)}$	Turn-Off Delay Time		-	115	-	ns
t_f	Fall Time		-	48	-	ns
E_{on}	Turn-On Switching Loss		-	2.1	-	mJ
E_{off}	Turn-Off Switching Loss		-	0.78	-	mJ
E_{ts}	Total Switching Loss		-	2.88	-	mJ

Electrical Characteristics of the IGBT (Continued)

Symbol	Parameter	Test Conditions	Min.	Typ.	Max	Unit
Q_g	Total Gate Charge	V_{CE} = 400 V, I_C = 60 A, V_{GE} = 15 V	-	189	284	nC
Q_{ge}	Gate to Emitter Charge		-	20	30	nC
Q_{gc}	Gate to Collector Charge		-	91	137	nC

Electrical Characteristics of the Diode T_C = 25°C unless otherwise noted

Symbol	Parameter	Test Conditions		Min.	Typ.	Max	Unit
V_{FM}	Diode Forward Voltage	I_F = 30 A	T_C = 25°C	-	2.1	2.7	V
			T_C = 175°C	-	1.7	-	
E_{rec}	Reverse Recovery Energy	I_F =30 A, di_F/dt = 200 A/μs	T_C = 175°C	-	79	-	uJ
t_{rr}	Diode Reverse Recovery Time		T_C = 25°C	-	30	39	ns
			T_C = 175°C	-	72	-	
Q_{rr}	Diode Reverse Recovery Charge		T_C = 25°C	-	44	62	nC
			T_C = 175°C	-	238	-	

Typical Performance Characteristics

Figure 1. Typical Output Characteristics

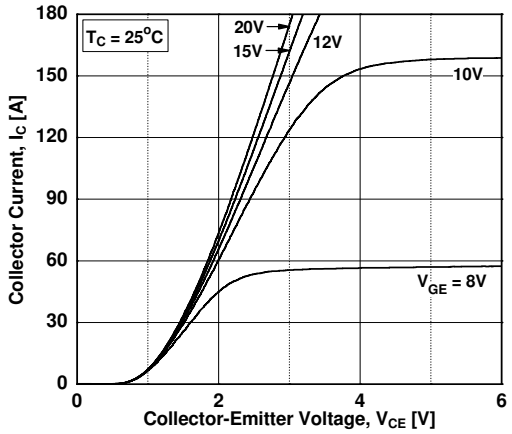


Figure 2. Typical Output Characteristics

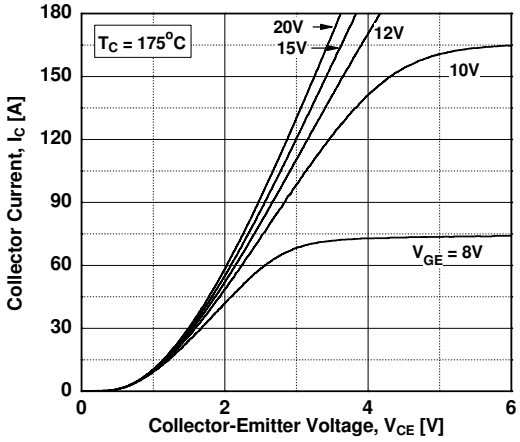


Figure 3. Typical Saturation Voltage Characteristics

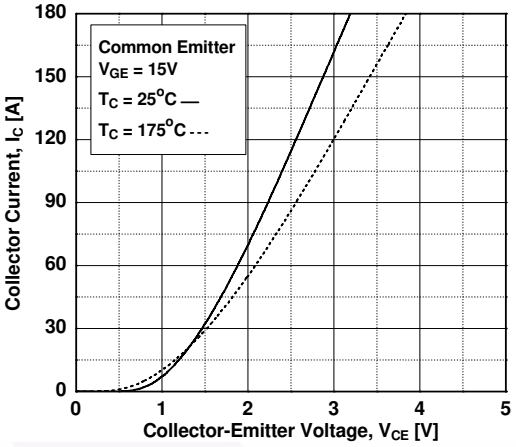


Figure 4. Transfer Characteristics

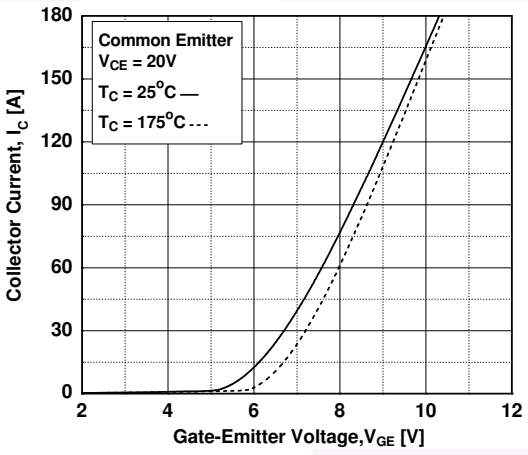


Figure 5. Saturation Voltage vs. Case Temperature at Variant Current Level

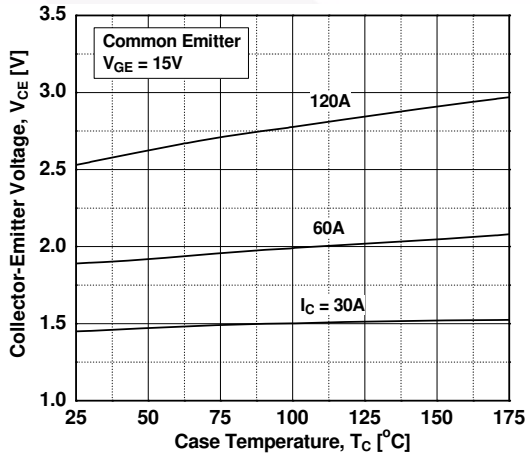
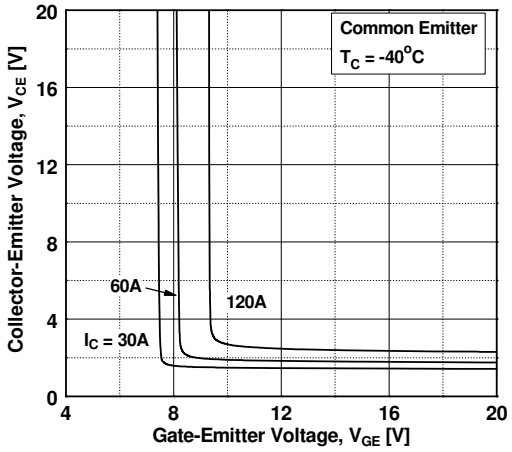


Figure 6. Saturation Voltage vs. V_{GE}



Typical Performance Characteristics

Figure 7. Saturation Voltage vs. V_{GE}

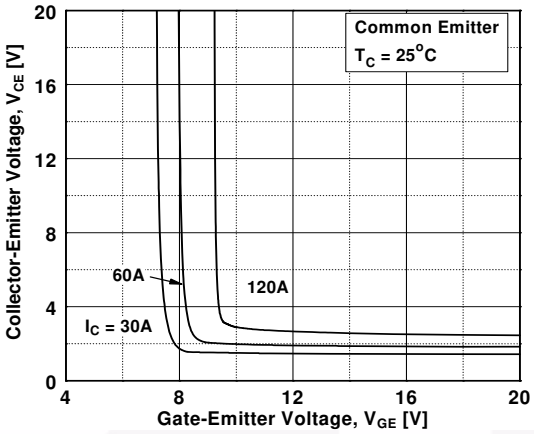


Figure 8. Saturation Voltage vs. V_{GE}

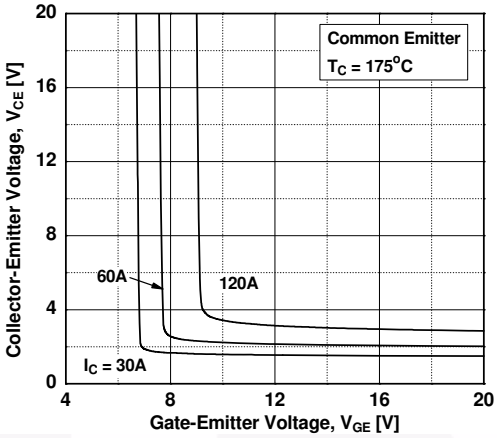


Figure 9. Capacitance Characteristics

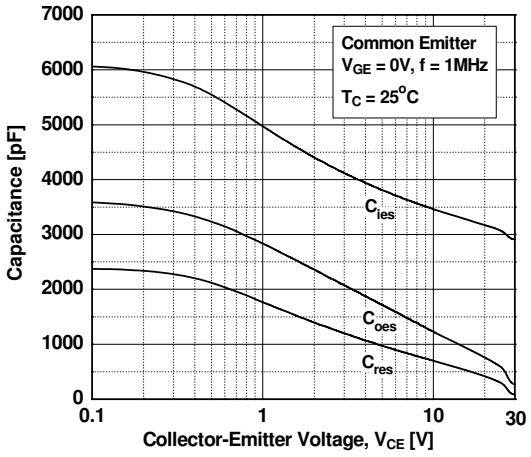


Figure 10. Gate charge Characteristics

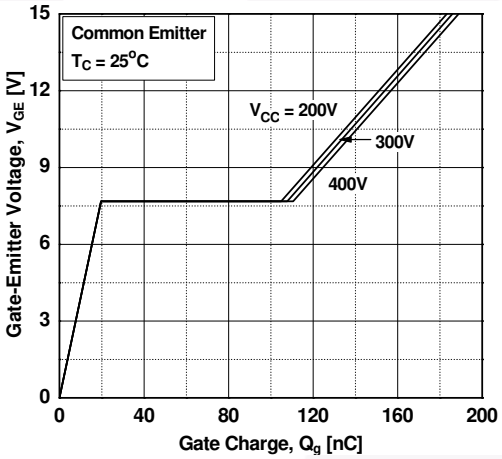


Figure 11. SOA Characteristics

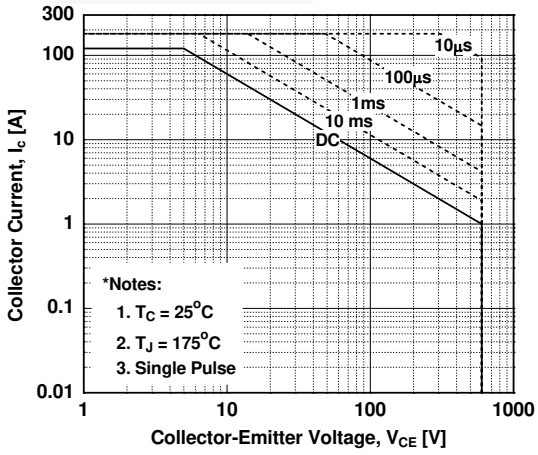
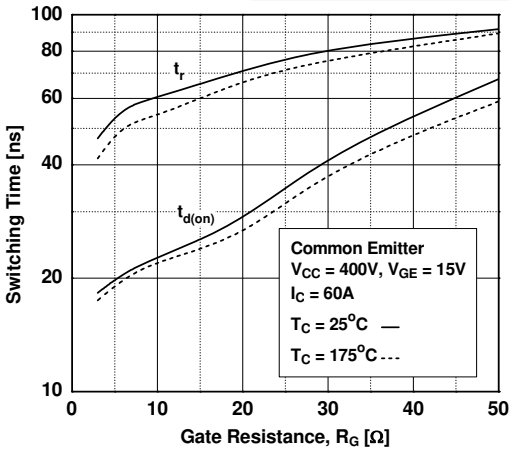


Figure 12. Turn-on Characteristics vs. Gate Resistance



Typical Performance Characteristics

Figure 13. Turn-off Characteristics vs. Gate Resistance

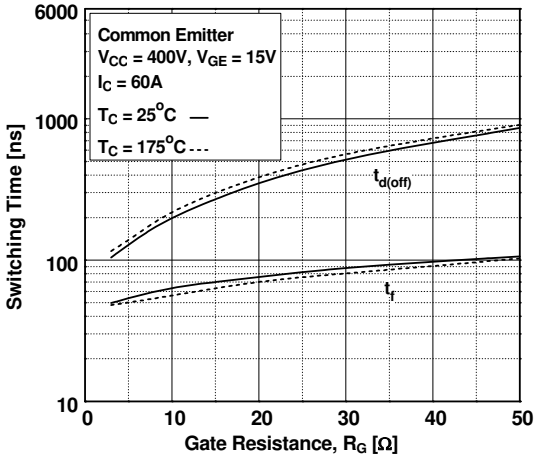


Figure 14. Turn-on Characteristics vs. Collector Current

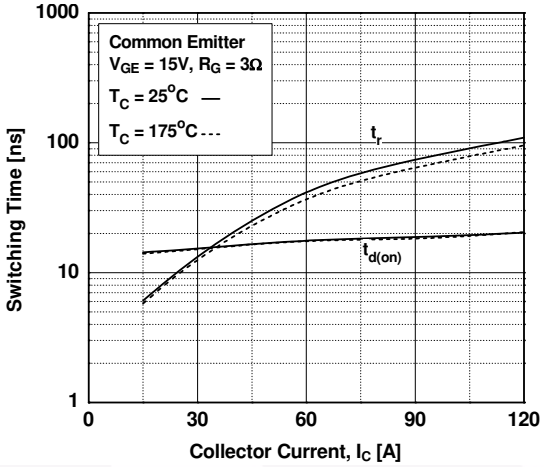


Figure 15. Turn-off Characteristics vs. Collector Current

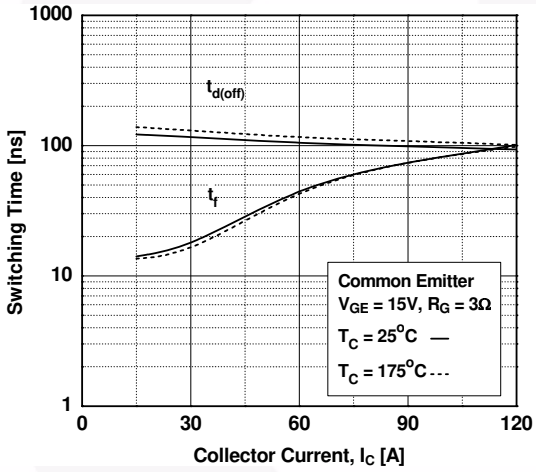


Figure 16. Switching Loss vs. Gate Resistance

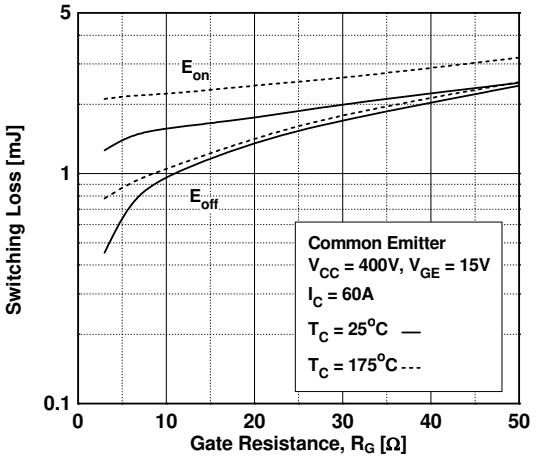


Figure 17. Switching Loss vs. Collector Current

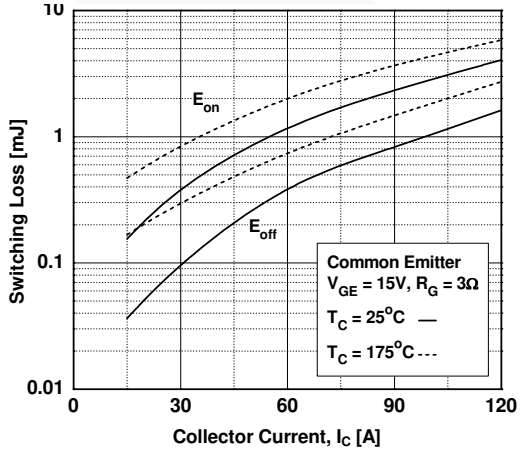
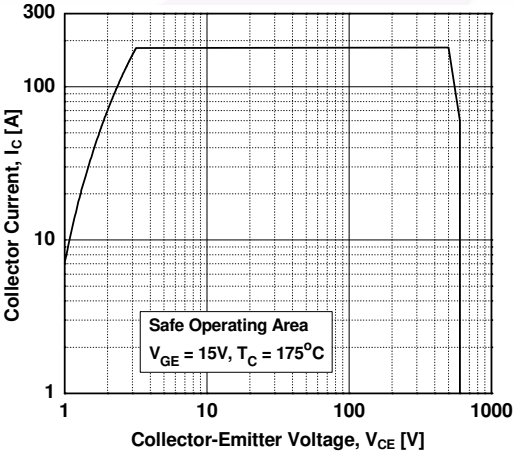


Figure 18. Turn off Switching SOA Characteristics



Typical Performance Characteristics

Figure 19. Current Derating

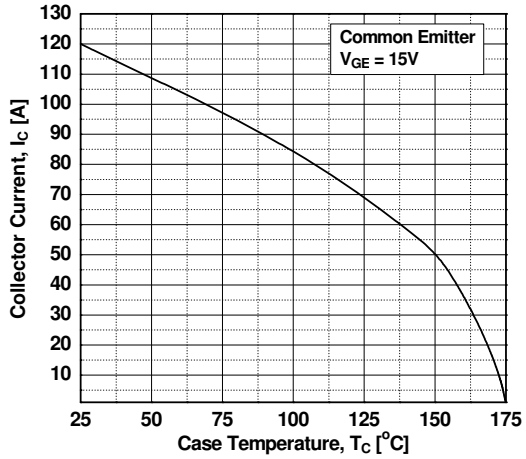


Figure 20. Load Current Vs. Frequency

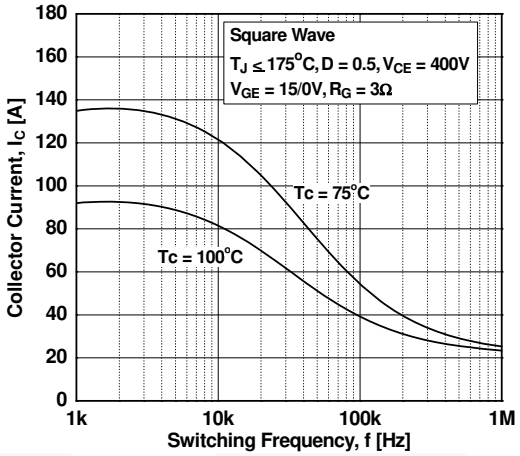


Figure 21. Forward Characteristics

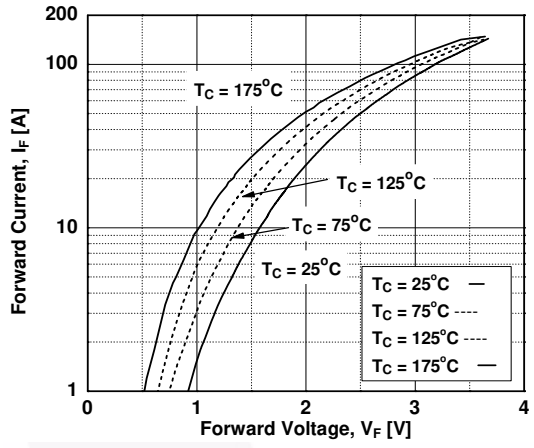


Figure 22. Reverse Current

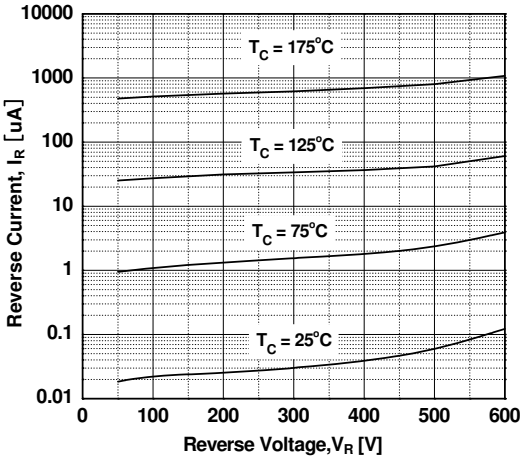


Figure 23. Stored Charge

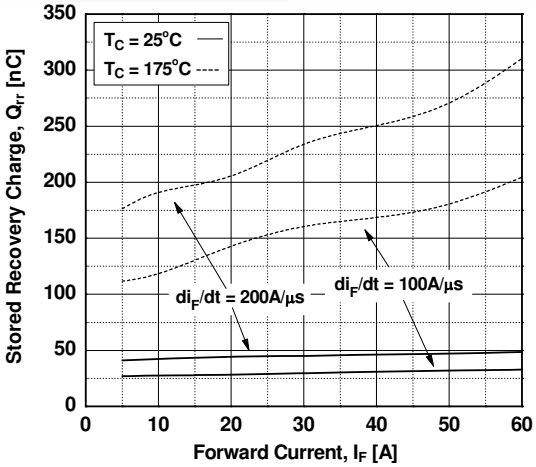
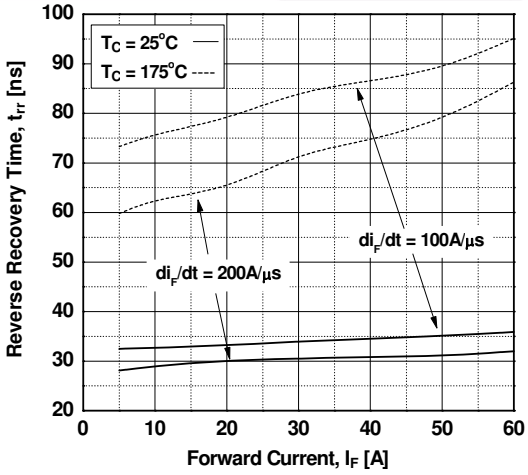


Figure 24. Reverse Recovery Time



Typical Performance Characteristics

Figure 25.Transient Thermal Impedance of IGBT

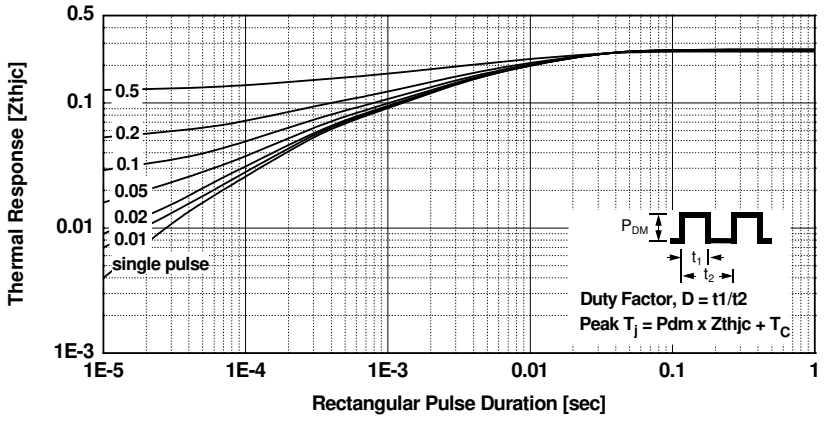
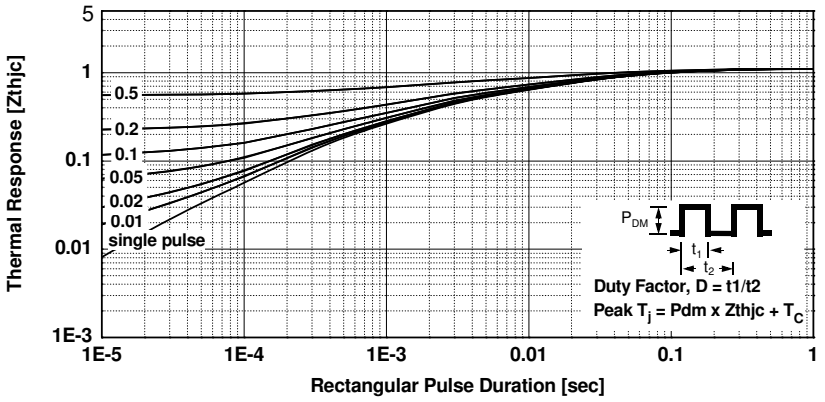
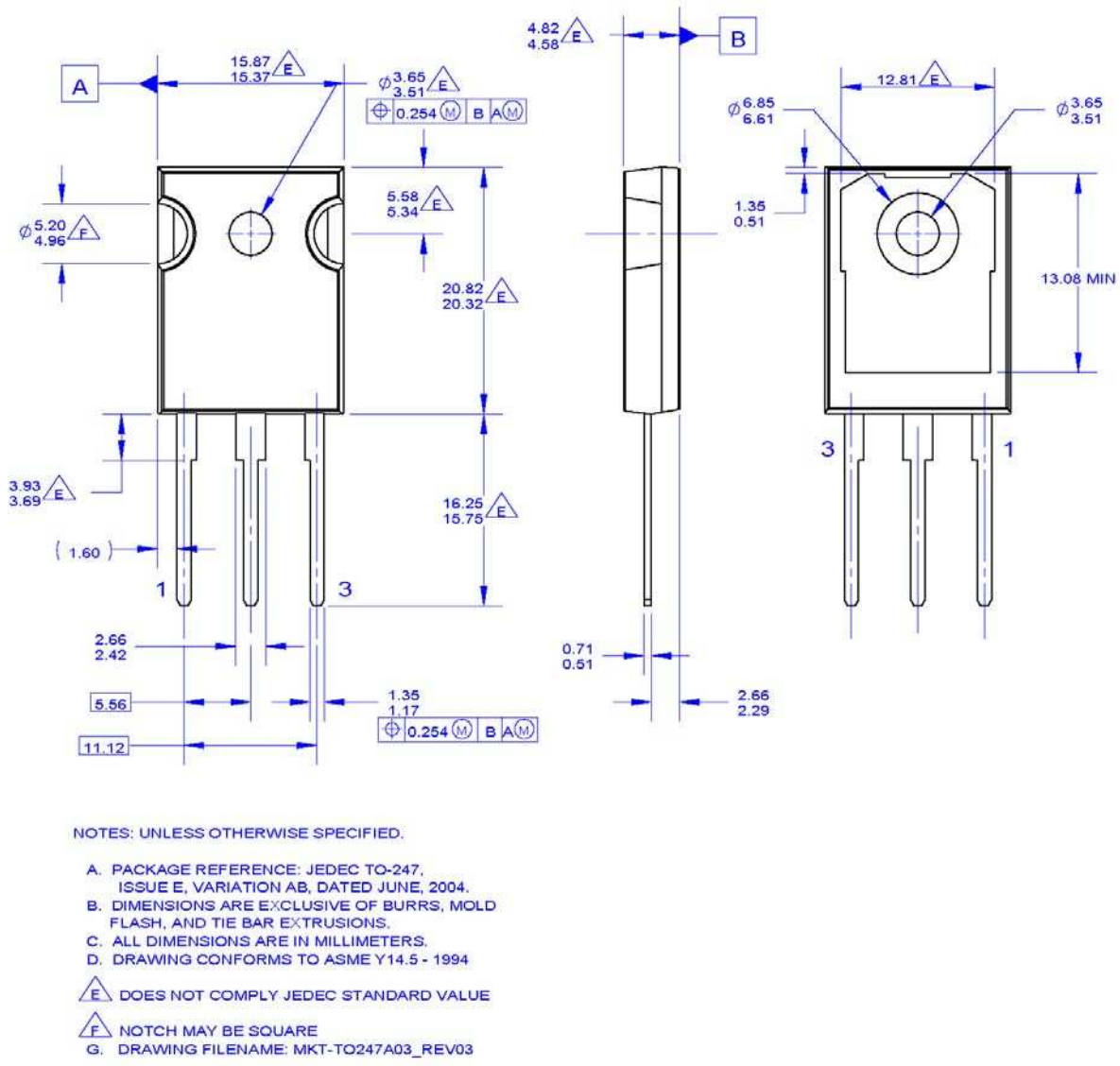


Figure 26.Transient Thermal Impedance of Diode



Mechanical Dimensions



NOTES: UNLESS OTHERWISE SPECIFIED.

A. PACKAGE REFERENCE: JEDEC TO-247, ISSUE E, VARIATION AB, DATED JUNE, 2004.
B. DIMENSIONS ARE EXCLUSIVE OF BURRS, MOLD FLASH, AND TIE BAR EXTRUSIONS.
C. ALL DIMENSIONS ARE IN MILLIMETERS.
D. DRAWING CONFORMS TO ASME Y14.5 - 1994

E. DOES NOT COMPLY JEDEC STANDARD VALUE

F. NOTCH MAY BE SQUARE

G. DRAWING FILENAME: MKT-TO247A03_REV03

Figure 27. TO-247 3L - TO-247,MOLDED,3 LEAD,JEDEC VARIATION AB

Package drawings are provided as a service to customers considering Fairchild components. Drawings may change in any manner without notice. Please note the revision and/or date on the drawing and contact a Fairchild Semiconductor representative to verify or obtain the most recent revision. Package specifications do not expand the terms of Fairchild's worldwide terms and conditions, specifically the warranty therein, which covers Fairchild products.

Always visit Fairchild Semiconductor's online packaging area for the most recent package drawings:

http://www.fairchildsemi.com/package/packageDetails.html?id=PN_TO247-003

FAIRCHILD SEMICONDUCTOR®

TRADEMARKS

The following includes registered and unregistered trademarks and service marks, owned by Fairchild Semiconductor and/or its global subsidiaries, and is not intended to be an exhaustive list of all such trademarks.

AccuPower™
AX-CAP®*
BitSiC™
Build it Now™
CorePLUS™
CorePOWER™
CROSSVOLT™
CTL™
Current Transfer Logic™
DEUXPEED®
Dual Cool™
EcoSPARK®
EfficentMax™
ESBC™
F®
Fairchild®
Fairchild Semiconductor®
FACT Quiet Series™
FACT®
FAST®
FastvCore™
FETBench™
FPS™
F-PFS™
FRFET®
Global Power Resource℠
GreenBridge™
Green FPS™
Green FPS™ e-Series™
G*max*™
GTO™
IntelliMAX™
ISOPLANAR™
Marking Small Speakers Sound Louder and Better™
MegaBuck™
MICROCOUPLER™
MicroFET™
MicroPak™
MicroPak2™
MillerDrive™
MotionMax™
mWSaver®
OptoHiT™
OPTOLOGIC®
OPTOPLANAR®
®
PowerTrench®
PowerXS™
Programmable Active Droop™
QFET®
QS™
Quiet Series™
RapidConfigure™
™
Saving our world, 1mW/W/kW at a time™
SignalWise™
SmartMax™
SMART START™
Solutions for Your Success™
SPM®
STEALTH™
SuperFET®
SuperSOT™-3
SuperSOT™-6
SuperSOT™-8
SupreMOS®
SyncFET™
Sync-Lock™
SYSTEM GENERAL ®*
TinyBoost®
TinyBuck®
TinyCalc™
TinyLogic®
TINYOPTO™
TinyPower™
TinyPWM™
TinyWire™
TranSiC™
TriFault Detect™
TRUECURRENT®*
µSerDes™
µSerDes™
UHC®
Ultra FRFET™
UniFET™
VCX™
VisualMax™
VoltagePlus™
XS™

*Trademarks of System General Corporation, used under license by Fairchild Semiconductor.

DISCLAIMER

FAIRCHILD SEMICONDUCTOR RESERVES THE RIGHT TO MAKE CHANGES WITHOUT FURTHER NOTICE TO ANY PRODUCTS HEREIN TO IMPROVE RELIABILITY, FUNCTION, OR DESIGN. FAIRCHILD DOES NOT ASSUME ANY LIABILITY ARISING OUT OF THE APPLICATION OR USE OF ANY PRODUCT OR CIRCUIT DESCRIBED HEREIN; NEITHER DOES IT CONVEY ANY LICENSE UNDER ITS PATENT RIGHTS, NOR THE RIGHTS OF OTHERS. THESE SPECIFICATIONS DO NOT EXPAND THE TERMS OF FAIRCHILD'S WORLDWIDE TERMS AND CONDITIONS, SPECIFICALLY THE WARRANTY THEREIN, WHICH COVERS THESE PRODUCTS.

LIFE SUPPORT POLICY

FAIRCHILD'S PRODUCTS ARE NOT AUTHORIZED FOR USE AS CRITICAL COMPONENTS IN LIFE SUPPORT DEVICES OR SYSTEMS WITHOUT THE EXPRESS WRITTEN APPROVAL OF FAIRCHILD SEMICONDUCTOR CORPORATION.

As used here in:

1. Life support devices or systems are devices or systems which, (a) are intended for surgical implant into the body or (b) support or sustain life, and (c) whose failure to perform when properly used in accordance with instructions for use provided in the labeling, can be reasonably expected to result in a significant injury of the user.
2. A critical component in any component of a life support, device, or system whose failure to perform can be reasonably expected to cause the failure of the life support device or system, or to affect its safety or effectiveness.

ANTI-COUNTERFEITING POLICY

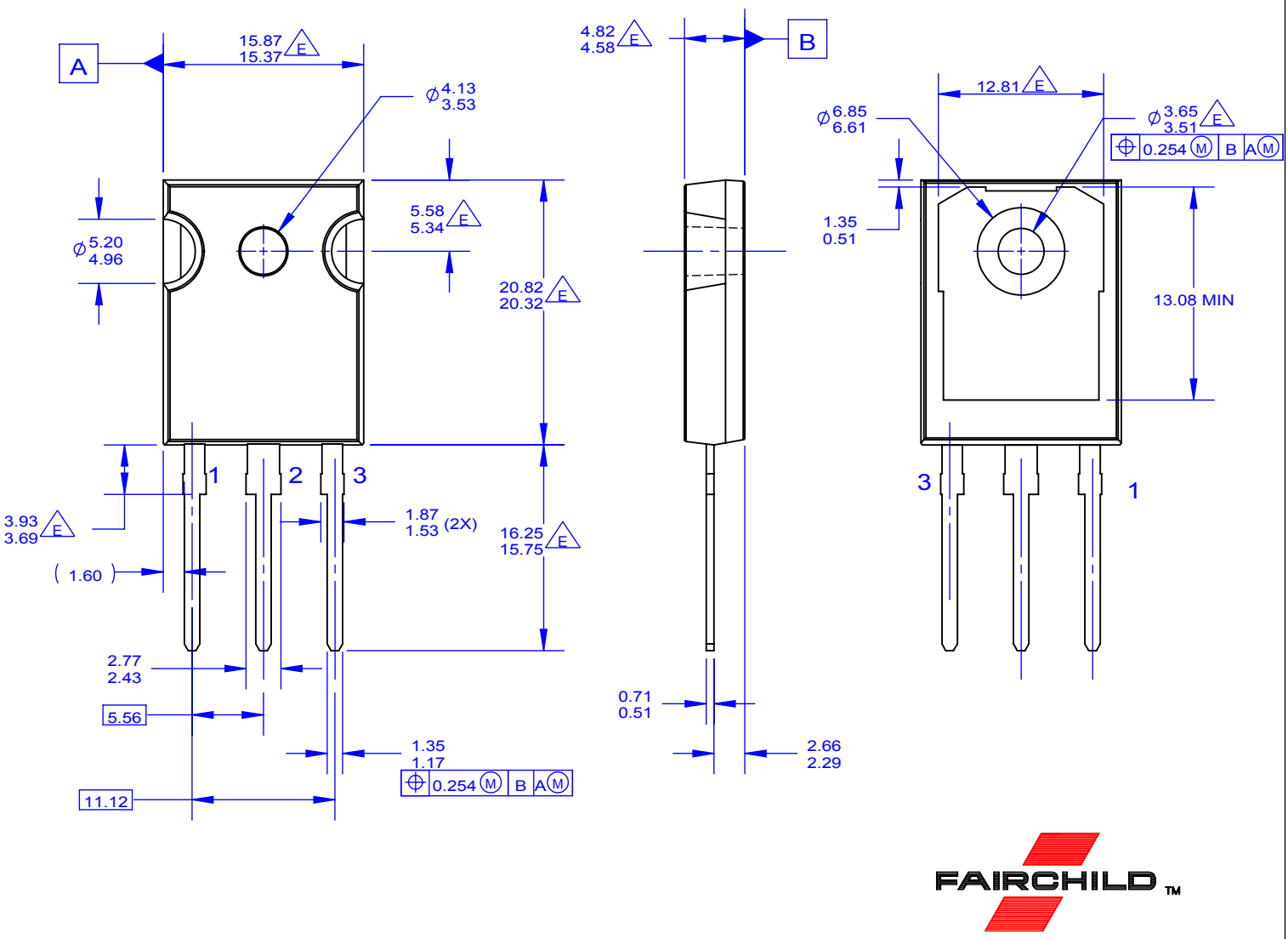
Fairchild Semiconductor Corporation's Anti-Counterfeiting Policy. Fairchild's Anti-Counterfeiting Policy is also stated on our external website, www.Fairchildsemi.com, under Sales Support.

Counterfeiting of semiconductor parts is a growing problem in the industry. All manufactures of semiconductor products are experiencing counterfeiting of their parts. Customers who inadvertently purchase counterfeit parts experience many problems such as loss of brand reputation, substandard performance, failed application, and increased cost of production and manufacturing delays. Fairchild is taking strong measures to protect ourselves and our customers from the proliferation of counterfeit parts. Fairchild strongly encourages customers to purchase Fairchild parts either directly from Fairchild or from Authorized Fairchild Distributors who are listed by country on our web page cited above. Products customers buy either from Fairchild directly or from Authorized Fairchild Distributors are genuine parts, have full traceability, meet Fairchild's quality standards for handing and storage and provide access to Fairchild's full range of up-to-date technical and product information. Fairchild and our Authorized Distributors will stand behind all warranties and will appropriately address and warranty issues that may arise. Fairchild will not provide any warranty coverage or other assistance for parts bought from Unauthorized Sources. Fairchild is committed to combat this global problem and encourage our customers to do their part in stopping this practice by buying direct or from authorized distributors.

PRODUCT STATUS DEFINITIONS
Definition of Terms

Datasheet Identification	Product Status	Definition
Advance Information	Formative / In Design	Datasheet contains the design specifications for product development. Specifications may change in any manner without notice.
Preliminary	First Production	Datasheet contains preliminary data; supplementary data will be published at a later date. Fairchild Semiconductor reserves the right to make changes at any time without notice to improve design.
No Identification Needed	Full Production	Datasheet contains final specifications. Fairchild Semiconductor reserves the right to make changes at any time without notice to improve the design.
Obsolete	Not In Production	Datasheet contains specifications on a product that is discontinued by Fairchild Semiconductor. The datasheet is for reference information only.

Rev. I66



FAIRCHILD ™

NOTES: UNLESS OTHERWISE SPECIFIED.

A. PACKAGE REFERENCE: JEDEC TO-247, ISSUE E, VARIATION AB, DATED JUNE, 2004.
B. DIMENSIONS ARE EXCLUSIVE OF BURRS, MOLD FLASH, AND TIE BAR EXTRUSIONS.
C. ALL DIMENSIONS ARE IN MILLIMETERS.
D. DRAWING CONFORMS TO ASME Y14.5 - 1994

E DOES NOT COMPLY JEDEC STANDARD VALUE
F. DRAWING FILENAME: MKT-TO247A03_REV04

PUBLICATION ORDERING INFORMATION

LITERATURE FULFILLMENT:
Literature Distribution Center for ON Semiconductor
19521 E. 32nd Pkwy, Aurora, Colorado 80011 USA
Phone: 303−675−2175 or 800−344−3860 Toll Free USA/Canada
Fax: 303−675−2176 or 800−344−3867 Toll Free USA/Canada
Email: orderlit@onsemi.com

N. American Technical Support: 800−282−9855 Toll Free USA/Canada
Europe, Middle East and Africa Technical Support:
Phone: 421 33 790 2910
Japan Customer Focus Center
Phone: 81−3−5817−1050

ON Semiconductor Website: **www.onsemi.com**

Order Literature: http://www.onsemi.com/orderlit

For additional information, please contact your local Sales Representative