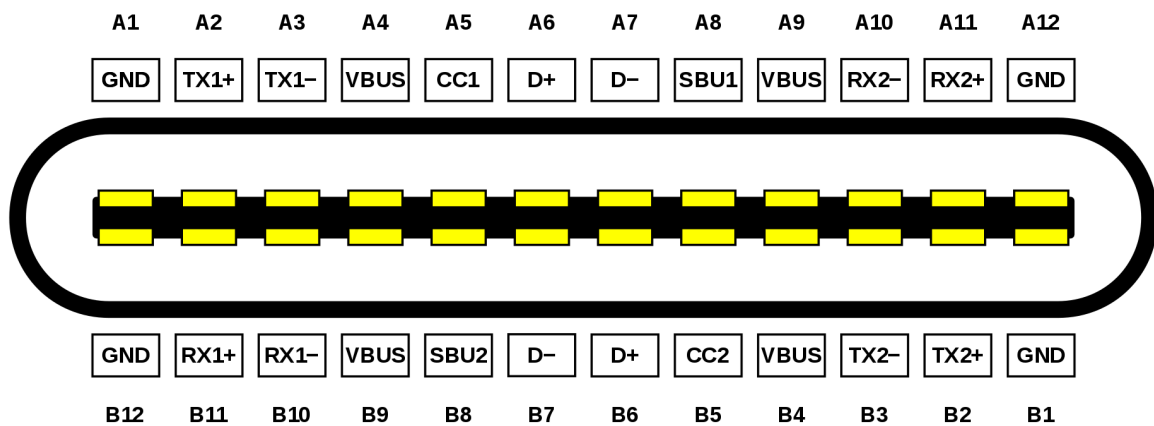




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USB-C receptacle A pin layout

Pin	Name	Description
A1	GND	Ground return
A2	SSTXp1	SuperSpeed differential pair #1, TX, positive
A3	SSTXn1	SuperSpeed differential pair #1, TX, negative
A4	V _{BUS}	Bus power
A5	CC1	Configuration channel
A6	Dp1	USB 2.0 differential pair, position 1, positive
A7	Dn1	USB 2.0 differential pair, position 1, negative
A8	SBU1	Sideband use (SBU)
A9	V _{BUS}	Bus power
A10	SSRXn2	SuperSpeed differential pair #4, RX, negative
A11	SSRXp2	SuperSpeed differential pair #4, RX, positive
A12	GND	Ground return

USB-C receptacle B pin layout

Pin	Name	Description
B12	GND	Ground return
B11	SSRXp1	SuperSpeed differential pair #2, RX, positive
B10	SSRXn1	SuperSpeed differential pair #2, RX, negative
B9	V _{BUS}	Bus power
B8	SBU2	Sideband use (SBU)
B7	Dn2	USB 2.0 differential pair, position 2, negative
B6	Dp2	USB 2.0 differential pair, position 2, positive
B5	CC2	Configuration channel
B4	V _{BUS}	Bus power
B3	SSTXn2	SuperSpeed differential pair #3, TX, negative
B2	SSTXp2	SuperSpeed differential pair #3, TX, positive
B1	GND	Ground return