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ETR0305 008

Low ESR Cap. Compatible Positive Voltage Regulators

■GENERAL DESCRIPTION

The XC6206 series are highly precise, low power consumption, 3 terminal, positive voltage regulators manufactured using CMOS and laser trimming technologies. The series provides large currents with a significantly small dropout voltage.

The XC6206 consists of a current limiter circuit, a driver transistor, a precision reference voltage and an error correction circuit. The series is compatible with low ESR ceramic capacitors. The current limiter's foldback circuit operates as a short circuit protection as well as the output current limiter for the output pin.

Output voltages are internally by laser trimming technologies. It is selectable in 0.1V increments within a range of 1.2V to 5.0V. SOT-23, SOT-89 and USP-6B packages are available.

APPLICATIONS

- Smart phones / Mobile phones
- Portable game consoles
- Digital still cameras / Camcorders
- Digital audio equipments
- Reference voltage sources
- Multi-function power supplies

■FEATURES

Maximum Output Current : 200mA (3.0V type)

Dropout Voltage : 250mV @ 100mA (3.0V type)

Maximum Operating Voltage : 6.0V

Output Voltage Range : 1.2V ~ 5.0V (0.1V increments)

Highly Accurate $:\pm 2\%@V_{OUT} \ge 1.5V$

<u>+</u>30mV@VouT<1.5V

(<u>+</u>1% @Vo∪т<u>≥</u>2.0V)

Low Power Consumption : 1.0µA (TYP.)

Low ESR Capacitor : Ceramic capacitor compatible
Protection : Current Limit Circuit Built-in

Operating Ambient Temperature : -40°C~ +85°C

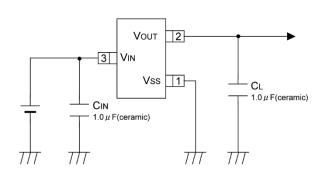
Packages : SOT-23

SOT-89

USP-6B

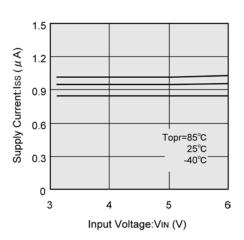
Environmentally Friendly: EU RoHS Compliant, Pb Free

■TYPICAL APPLICATION CIRCUIT

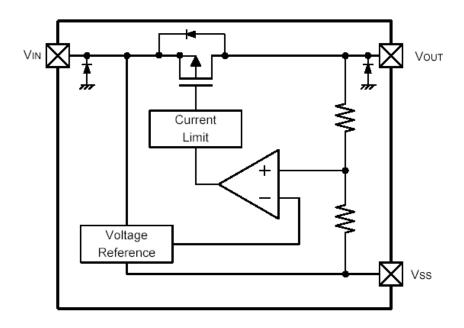


■TYPICAL PERFORMANCE CHARACTERISTICS

XC6206P302



■BLOCK DIAGRAM



^{*}Diodes inside the circuit are an ESD protection diode and a parasitic diode.

■ PRODUCT CLASSIFICATION

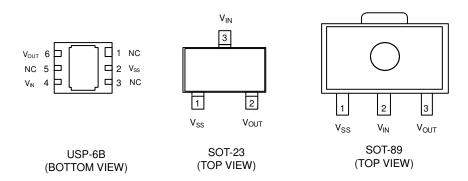
Ordering Information

 $\underline{\mathsf{XC6206P} \underbrace{12345} - \underline{6}}^{(*1)}$

DESIGNATOR	ITEM	SYMBOL	DESCRIPTION
12	Output Voltage	12~50	e.g. Vou⊤: 3.0V→①=3, ②=0
3	Acquiracy	2	±2% (V _{OUT} ≥1.5V), ±30mV (V _{OUT} <1.5V)
3	Accuracy	1	±1% (Vouт≧2.0V)
		MR	SOT-23 (3,000pcs/Reel)
		MR-G	SOT-23 (3,000pcs/Reel)
45-6	Packages	PR	SOT-89 (1,000pcs/Reel)
40-0	(Order Unit)	PR-G	SOT-89 (1,000pcs/Reel)
		DR	USP-6B (3,000pcs/Reel)
		DR-G	USP-6B (3,000pcs/Reel)

^(*1) The "-G" suffix denotes Halogen and Antimony free as well as being fully EU RoHS compliant.

■PIN CONFIGURATION



^{*}The dissipation pad for the USP-6B package should be solder-plated in recommended mount pattern and metal masking so as to enhance mounting strength and heat release.

■ PIN ASSIGNMENT

Р	IN NUMBER		PIN NAME	FUNCTIONS
SOT-23	SOT-89	USP-6B	FIN NAIVIL	1 0110 110113
1	1	2	Vss	Ground
3	2	4	Vin	Power Input
2	3	6	Vout	Output
-	-	1, 3, 5	NC	No Connection

■ ABSOLUTE MAXIMUM RATINGS

Ta=25°C

PARAMET	ER	SYMBOL RATINGS		UNITS			
Input Volta	age	VIN	-0.3 ~ +7.0	V			
Output Cui	rent	lout	500 (*1)	mA			
Output Vol	tage	Vout	$-0.3 \sim V_{IN} + 0.3$	V			
	SOT-23		250				
	501-23		500(40mm x 40mm Standard board) (*2)				
Dawey Dissipation	007.00	COT 00	COT 00	COT 00	Pd	500	\
Power Dissipation	SOT-89	Fu	Fu	1000(40mm x 40mm Standard board) (*2)	- mW		
	LICD CD		120				
	USP-6B		1000(40mm x 40mm Standard board) (*2)	1			
Operating Ambient	Operating Ambient Temperature Topr		- 40 ~ + 85	°C			
Storage Temp	erature	Tstg	- 55 ~ + 125	°C			

 $^{^{(*1)}}$ Iout \leq Pd / (Vin-Vout)

The mounting condition is please refer to PACKAGING INFORMATION.

If the pad needs to be connected to other pins, it should be connected to the pin number 4 (V_{IN}) .

^(*2) The power dissipation figure shown is PCB mounted and is for reference only.

■ELECTRICAL CHARACTERISTICS

Ta=25°C

PARAMETER	SYMBOL	CONE	ITIONS	MIN.	TYP.	MAX.	UNITS	CIRCUIT
Output Voltage		Іонт=30mA	V _{OUT(T)} <1.5V	-0.03		+0.03		
(Standard)(*Ž)	V _{OUT(E)} (*3)	IOUT=3UIIIA	V _{OUT(T)} ≧1.5V	×0.98	V _{OUT(T)} (*4)	×1.02	V	2
Output Voltage (High Accuracy)(*2)	V OUT (E)	Іоит=30mА	V _{OUT(T)} ≧2.0V	×0.99	VOUI(I)	×1.01	V	(2)
Supply Current	I _{DD}			-	1.0	3.0	μA	1
Load Regulation	ΔVουτ	V _{OUT(T)} ≦1.8 1mA≦I _{OUT} ≦		_	_	E-1 ^(*5)	mV	2
Load Negulation	AVOUT	V _{OUT(T)} >1.8V 1mA≦I _{OUT} ≦		_	-	E-1(*)	mv	∠
Dropout Voltage 1	Vdif1 ^(*6)	I _{OUT} =30mA		-	E-2 ^(*5)			
Dropout Voltage 2	Vdif2 ^(*6)	V _{OUT(T)} ≦1.8	V, I _{OUT} =60mA	_	- E-3 ^(*5)		mV	2
Dropout voltage 2	Vull2	V _{OUT(T)} >1.8V	, I _{OUT} =100mA	_				
Line Regulation	ΔV _{OUT} /	Vout(T)<4.5V Vout(T)+1.0V Iout=30mA	/, /≦V _{IN} ≦6.0V,		0.05	0.25	%/V	2
Line Negulation	(ΔV _{IN} • V _{OUT})	V _{OUT(T)} ≧4.5V, 5.5V≦V _{IN} ≦6.0V, I _{OUT} =30mA		-	0.00	0.20	76/ V	(2)
Maximum Output Current	IOUTMAX	V _{OUT} ≧V _{OUT(}	V _{OUT} ≧V _{OUT(E)} × 0.9		-	-	mA	2
Short Circuit Current	Іsноят	V _{OUT} =V _{SS}		-	E-5 ^(*5)	-	mA	2
Input Voltage	Vin			1.8	-	6.0	V	2
Output Voltage Temperature Characteristics	ΔV _{OUT} / (ΔTopr • V _{OUT})	l _{о∪т} =30mA, -40°C≦Topr	≦85°C	-	±100	-	ppm/°C	2

^{*1:} Unless otherwise stated, $V_{IN} = V_{OUT(T)} + 1.0V$

 V_{OUT1} : A voltage equal to 98% of the output voltage whenever an amply stabilized $\{V_{\text{OUT(T)}} + 1.0V\}$ is input with each I_{OUT} .

V_{IN1}: The input voltage when V_{OUT1} appears as input voltage is gradually decreased.

^{*2: (}Standard): $\pm 2\%$ (1.5V \leq V_{OUT(T)}) , ± 0.03 V (1.5V>V_{OUT(T)}) (High Accuracy): $\pm 1\%$ (2.0V \leq V_{OUT(T)})

^{*3:} V_{OUT(E)} :Effective output voltage.

^{*4:} V_{OUT(T)} :Nominal voltage

^{*5:} For E-1,E-2,E-3,E-4,E-5, Please refer to Electrical Characteristics Chart.

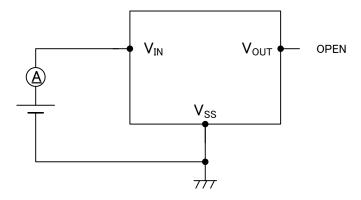
^{*6:} Vdif =V_{IN1} -V_{OUT1}

^{*7:} The low ESR capacitors use that is more than 1.0µF as C_L is possible.

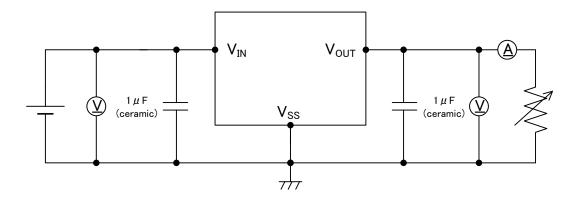
	E-1	E-	·2	E	<u>-</u> 3	E-4	E-5
NOMINAL VOLTAGE	LOAD REGULATION	DROF VOLT/	AGE1	VOLT	POUT AGE2	MAX. OUTPUT CURRENT	SHORT CURRENT
	∠Vout (mV)	V _{dif1} ((mV)	IOUTMAX (mA)	I _{SHORT} (mA)
V _{OUT(T)}	MAX.	TYP.	MAX.	TYP.	MAX.	MIN.	TYP.
1.2		460	760	700	960		
1.3	40	400	650	700	300	60	180
1.4		350	590	F00	000	60	
1.5		300	510	580	860		
1.6		250	450	450	810		155
1.7	45	200	410	450	610	80	
1.8		150	390			60	
1.9					780		
2.0							130
2.1							
2.2	50					120	
2.3				350			
2.4		100	370				
2.5					710		
2.6							
2.7	55					150	
2.8							
2.9							
3.0							
3.1	60						
3.3	00						
3.4							
3.5		75	350	250	680	200	
3.6							
3.7	65						100
3.8							
3.9							
4.0							
4.1							
4.2	70						
4.3							
4.4		60	320	200	630		
4.5		00	320	200	000	250	
4.6							
4.7	75						
4.8							
4.9							
5.0	80	50	290	175	600		

■TEST CIRCUITS

Circuit ①



Circuit 2

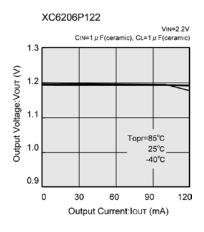


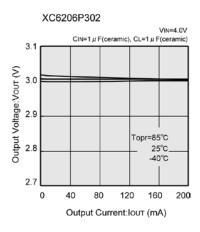
■NOTES ON USE

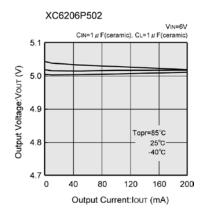
- 1. For temporary, transitional voltage drop or voltage rising phenomenon, the IC is liable to malfunction should the ratings be exceeded.
- 2. Where wiring impedance is high, operations may become unstable due to noise and/or phase lag depending on output current. Please strengthen V_{IN} and V_{SS} wiring in particular
- 3. Please wire the input capacitor (C_{IN}) and the output capacitor (C_L) as close to the IC as possible.
- 4. Capacitances of these capacitors (C_{IN}, C_L) are decreased by the influences of bias voltage and ambient temperature. Care shall be taken for capacitor selection to ensure stability of phase compensation from the point of ESR influence.
- 5. When it is used in a quite small input / output dropout voltage, output may go into unstable operation. Please test it thoroughly before using it in production.
- 6. Torex places an importance on improving our products and their reliability. We request that users incorporate fail-safe designs and post-aging protection treatment when using Torex products in their systems.

■TYPICAL PERFORMANCE CHARACTERISTICS

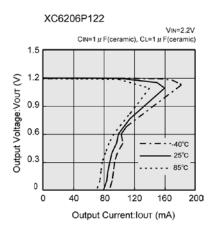
(1) Output Voltage vs. Output Current

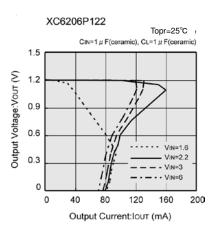


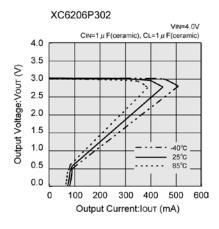


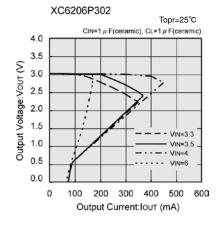


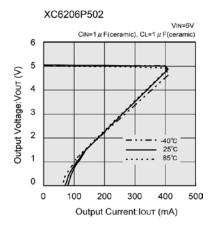
(2) Current Limit

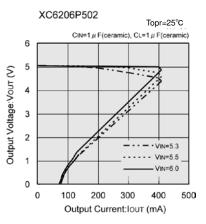








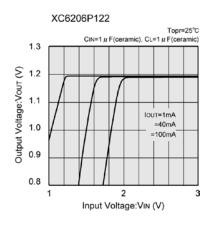


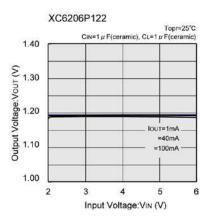


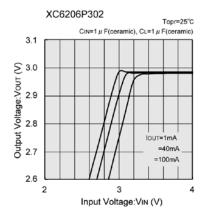
XC6206 Series

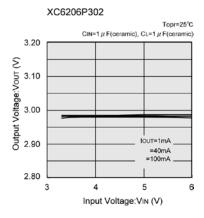
■TYPICAL PERFORMANCE CHARACTERISTICS (Continued)

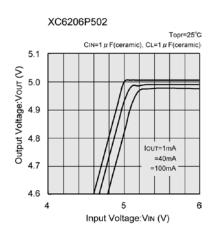
(3) Output Voltage vs. Input Voltage

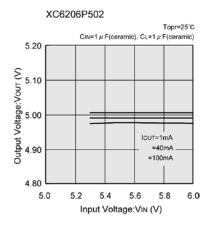




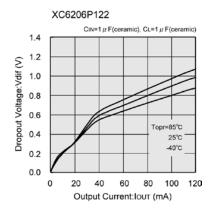


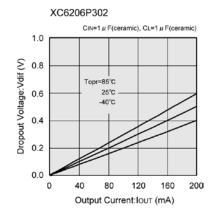


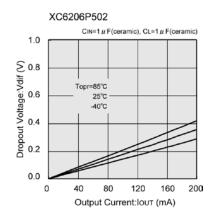




(4) Dropout Voltage vs. Output Current

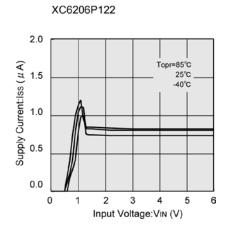


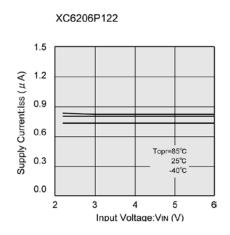


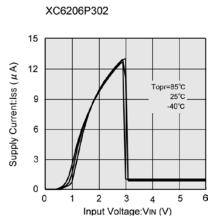


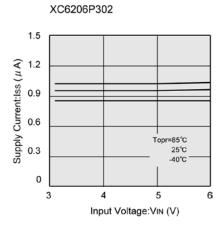
■TYPICAL PERFORMANCE CHARACTERISTICS (Continued)

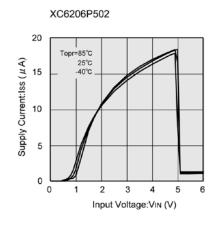
(5) Supply Current vs. Input Voltage

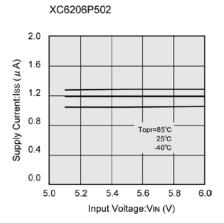




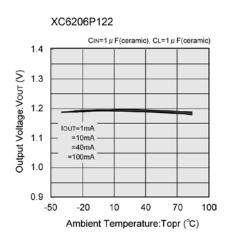


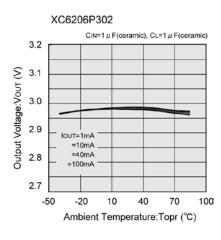


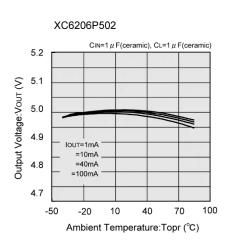




(6) Output Voltage vs. Ambient Temperature



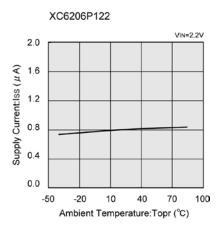


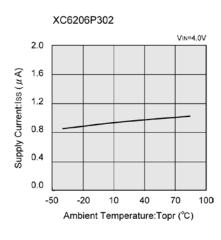


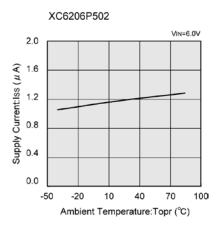
XC6206 Series

■TYPICAL PERFORMANCE CHARACTERISTICS (Continued)

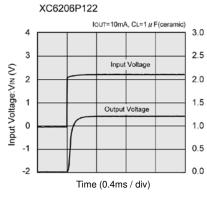
(7) Output Voltage vs. Ambient Temperature

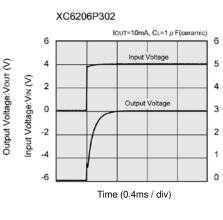


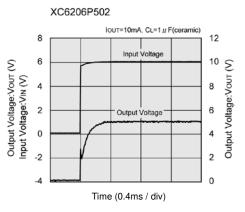




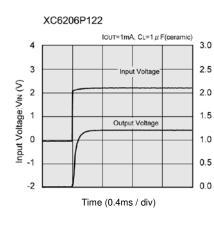
(8) Input Transient Response 1

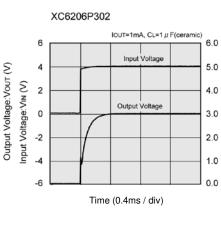


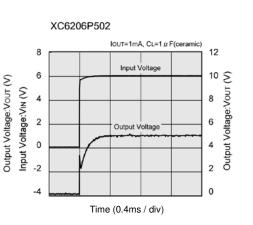




XC6206P122

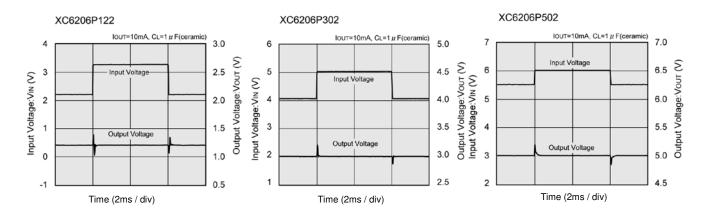


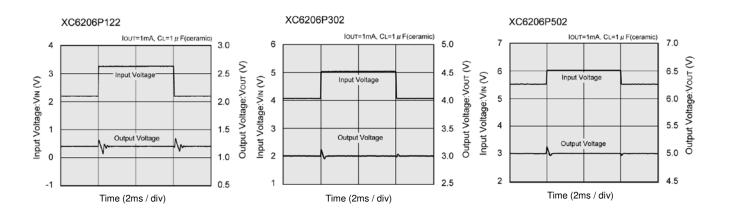




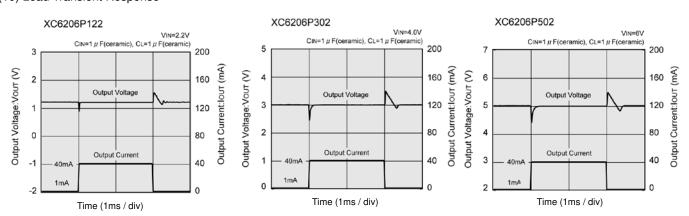
■TYPICAL PERFORMANCE CHARACTERISTICS (Continued)

(9) Input Transient Response 2



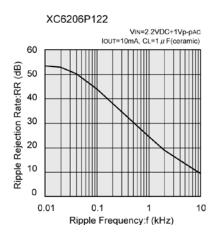


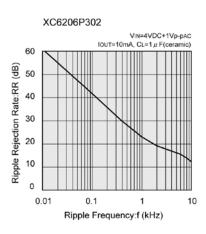
(10) Load Transient Response

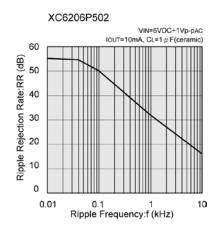


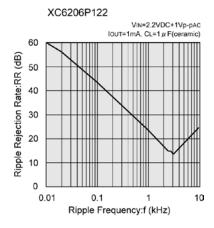
■TYPICAL PERFORMANCE CHARACTERISTICS (Continued)

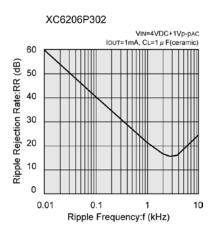
(11) Ripple Rejection Rate

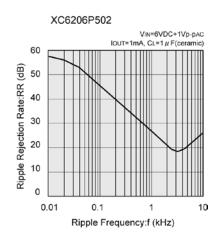












■ PACKAGING INFORMATION

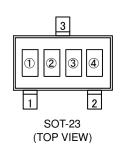
For the latest package information go to, www.torexsemi.com/technical-support/packages

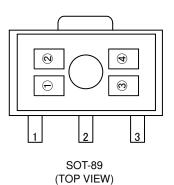
PACKAGE	OUTLIN / LAND PATTERN	THERMAL CHARACTERISTICS		
SOT-23	SOT-23 PKG	Standard Board	SOT-23 Power Dissipation	
SOT-89	SOT-89 PKG	Standard Board	SOT-89 Power Dissipation	
USP-6B	USP-6B PKG	Standard Board	USP-6B Power Dissipation	

XC6206 Series

■MARKING RULE

●SOT-23, SOT-89





① represents product number

MARK	PRODUCT SERIES
6	XC6206P****

2 represents 3 pins regulator

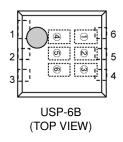
MA	MARK				
VOLTAGE=0.1 ~ 3.0V	VOLTAGE=3.1 ~ 6.0V	PRODUCT SERIES			
5	6	XC6206P****			

3 represents output voltage

MARK	VC	DLTAGE ((V)	MARK	OUTPL	JT VOLTA	AGE (V)
0	-	3.1	-	F	1.6	4.6	-
1	-	3.2	1	Н	1.7	4.7	-
2	-	3.3	ı	K	1.8	4.8	-
3	-	3.4	ı	L	1.9	4.9	-
4	-	3.5	1	М	2.0	5.0	-
5	-	3.6	-	N	2.1	-	-
6	-	3.7	ı	Р	2.2	ı	-
7	-	3.8	ı	R	2.3	ı	-
8	-	3.9	ı	S	2.4	ı	-
9	-	4.0	-	Т	2.5	-	-
Α		4.1	-	U	2.6	-	-
В	1.2	4.2	-	V	2.7	-	-
С	1.3	4.3	-	Х	2.8	-	-
D	1.4	4.4	1	Υ	2.9	1	-
Е	1.5	4.5	-	Z	3.0	-	-

4 represents production lot number 0 to 9, A to Z repeated. (G, I, J, O, Q, W excluded)

●USP-6B



①② represents product number

MA	PRODUCT SERIES	
1	2	PRODUCT SERIES
0	6	XC6206P***D*

3 represents 3 pins regulator

MARK	PRODUCT SERIES
Р	XC6206P***D*

45 represents output voltage

MAI	RK	OUTBUT VOLTAGE(V)	PRODUCT SERIES
4	5	OUTPUT VOLTAGE(V)	PRODUCT SERIES
3	3	3.3	XC6206P33*D*
5	0	5.0	XC6206P50*D*

6 represents production lot number

0 to 9, A to Z repeated. (G, I, J, O, Q, W excluded)

- 1. The product and product specifications contained herein are subject to change without notice to improve performance characteristics. Consult us, or our representatives before use, to confirm that the information in this datasheet is up to date.
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