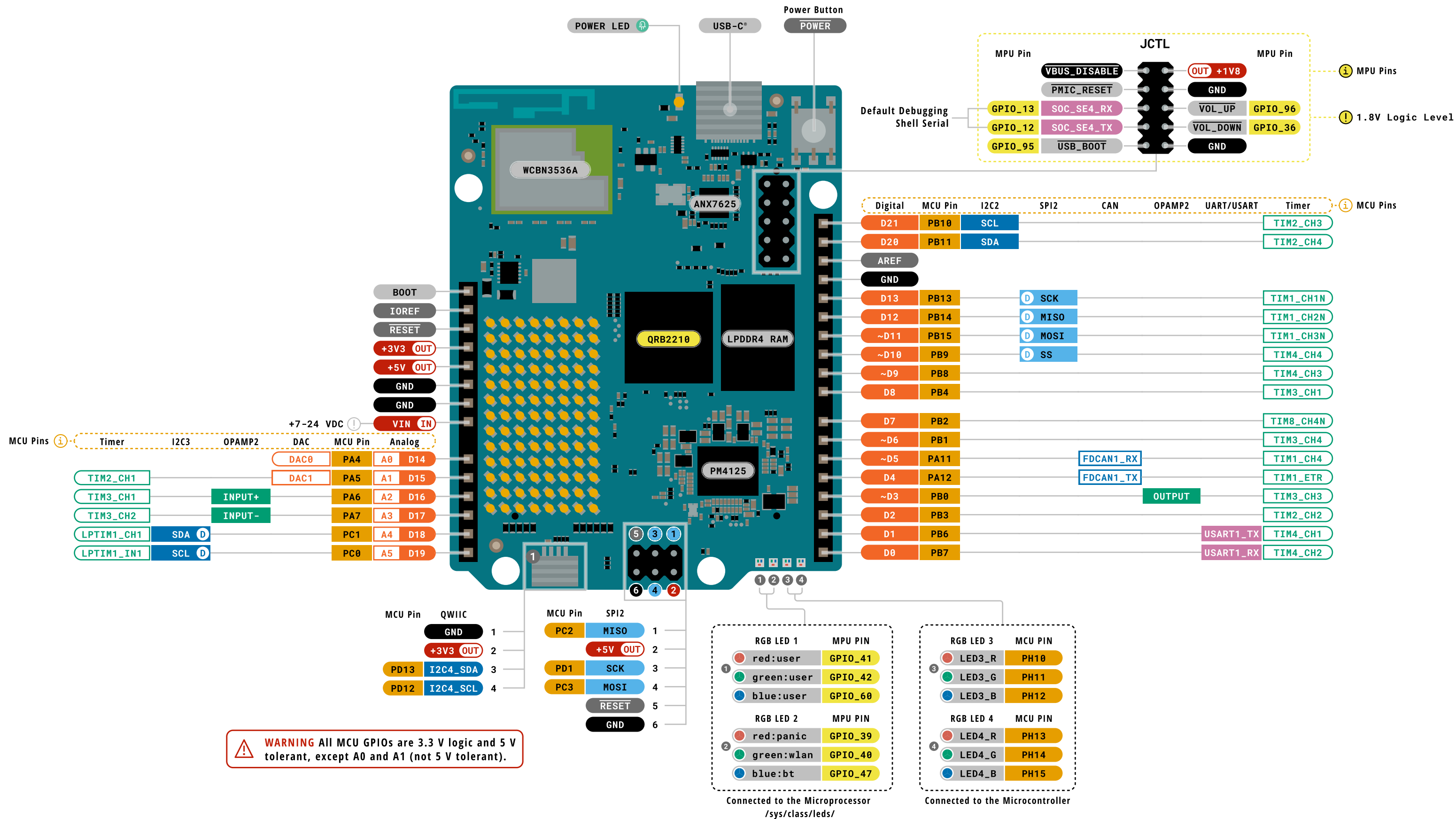




EN: This Datasheet/Document is presented by the manufacturer.

Please visit our website for pricing and availability at www.hestore.hu.



Legend:

Power

Ground

Power Input

Power Output

GPIO Digital External

Analog External

Main Part

Secondary Part

Internal Component

Other Pins (Reset, System Control, Debugging)

I2C

SPI

UART/USART

Other SERIAL

Analog

PWM/Timer

Default

Default

Default

Default

Default

Default

LED

RGB LED

CIPO/COPI have previously been referred ot as MISO/MOSI

JSPI and JDIGITAL SPI share the SPI2 MCU peripheral (Can not be used simultaneously as SPI)

WARNING A0 and A1 are not 5V tolerant

WARNING JCTL pins are 1.8V logic only

ARDUINO

SKU code: ABX00162-ABX00173

Full Pinout - Page 1 of 4

Last update: 30 Sept, 2025

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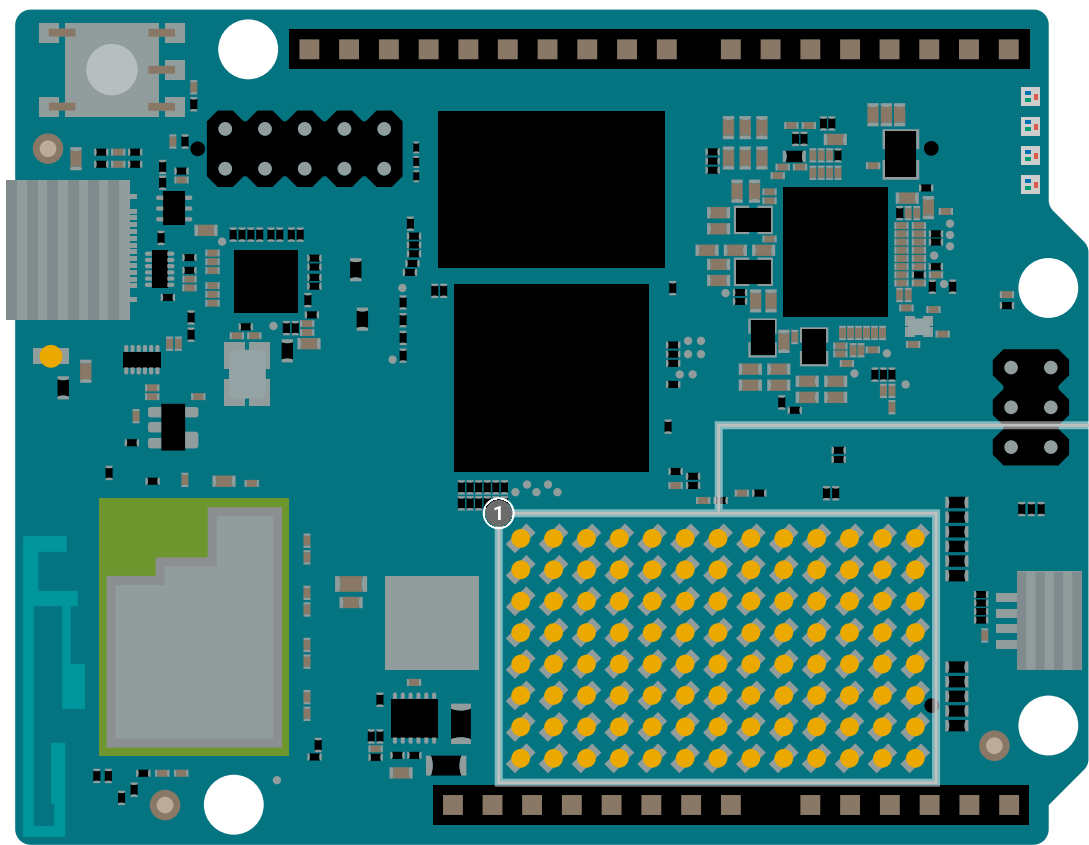
WARNING!

Advanced Section

The following information is for advanced use only and
may not be officially supported by Arduino software



LED Matrix 8x13



1	2	3	4	5	6	7	8	9	10	11	12	13
14	15	16	17	18	19	20	21	22	23	24	25	26
27	28	29	30	31	32	33	34	35	36	37	38	39
40	41	42	43	44	45	46	47	48	49	50	51	52
53	54	55	56	57	58	59	60	61	62	63	64	65
66	67	68	69	70	71	72	73	74	75	76	77	78
79	80	81	82	83	84	85	86	87	88	89	90	91
92	93	94	95	96	97	98	99	100	101	102	103	104

Legend :

- Power

Ground
- IN

Power Input

OUT

Power Output

- GPIO Digital External

Analog External

Main Part

Secondary Part

Internal Component

Other Pins (Reset, System Control, Debugging)

- I2C

SPI

UART/USART

Other SERIAL

Analog

PWM/Timer
- D

Default

D

Default

D

Default

D

Default

D

Default

- LED

RGB LED



SKU code: ABX00162-ABX00173
Full Pinout - Page 3 of 4
Last update: 2 Sept, 2025

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JMISC

PD2	MCU_SDMMC1_CMD	2	1	MCU_PSSI_D0	PC6
PE2	MCU_TRACE_CLK	4	3	MCU_PSSI_D1	PC7
PE3	MCU_TRACE_D0	6	5	MCU_PSSI_D2	PC8
PE5	MCU_TRACE_D2	8	7	MCU_PSSI_D3	PC9
PE6	MCU_TRACE_D3	10	9	MCU_PSSI_D4	PE4
PE7	MCU_PE7	12	11	MCU_PSSI_D5	PI4
PE8	MCU_PE8	14	13	MCU_PSSI_D6	PI6
PF14	MCU_I2C4_SCL	16	15	MCU_PSSI_D7	PI7
PF15	MCU_I2C4_SDA	18	17	MCU_PSSI_PDCK	PD9
PA3	MCU_OPAMP1_VOUT	20	19	MCU_PSSI_RDY	PI5
PA0	MCU_OPAMP1_VINP	22	21	MCU_PSSI_DE	PD8
PA1	MCU_OPAMP1_VINM	24	23	MCU_MCO	PA8
	GND	26	25	MCU_CRS_SYNC	PA10
	EAR_P_R	28	27	GND	
	EAR_M_R	30	29	MIC2_INP	
	LINEOUT_P	32	31	MIC2_INM	
	LINEOUT_M	34	33	MIC2_BIAS	
	HPH_L	36	35	GND	
	HPH_R	38	37	SOC_GPIO_0_SE0	
	HPH_REF	40	39	SOC_GPIO_1_SE0	
	HS_DET	42	41	SOC_GPIO_2_SE0	
	GND	44	43	SOC_GPIO_3_SE0	
	SOC_GPIO_98	46	45	SOC_GPIO_86_SE0	
	SOC_GPIO_99	48	47	SOC_GPIO_82_SE0	
	SOC_GPIO_100	50	49	SOC_GPIO_18	
	SOC_GPIO_101	52	51	SOC_GPIO_28	
	+5V USB OUT	54	53	OUT +3V3	
	+5V USB OUT	56	55	OUT +3V3	
	GND	58	57	OUT +1V8	
	VBAT IN	60	59	IN VCOIN	

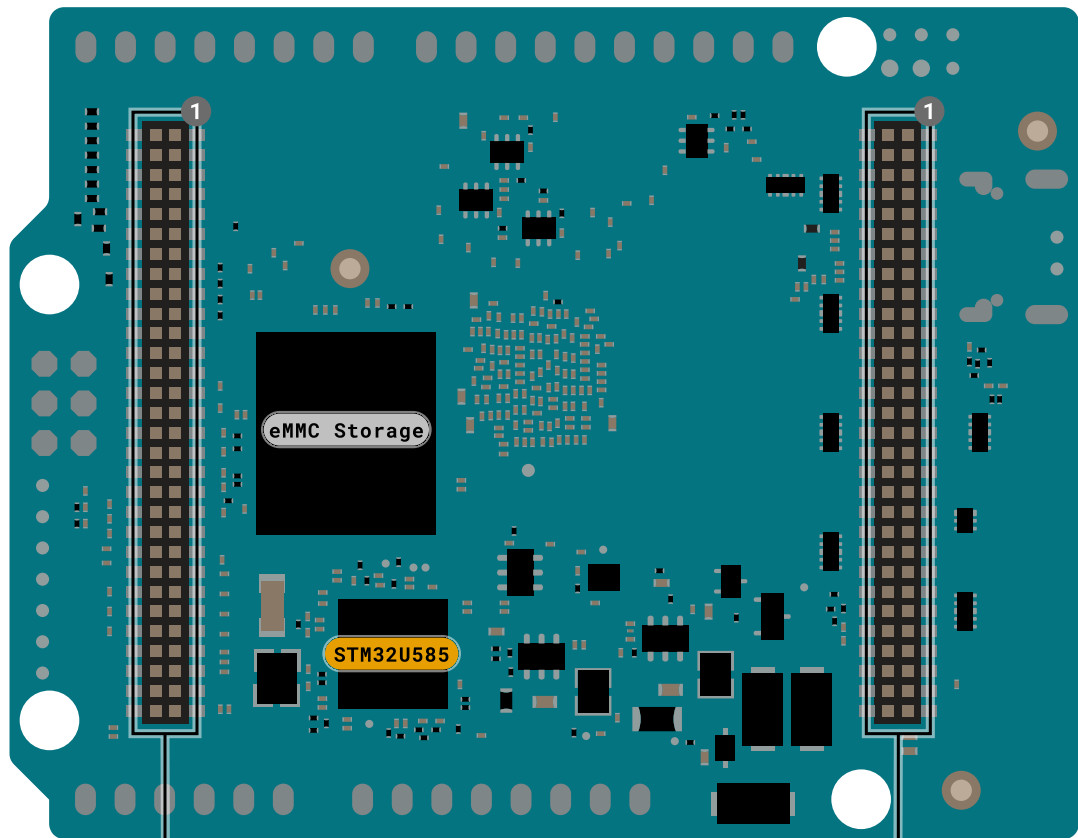
! These pins can not be used as regular GPIOs.

MCU Pins

3.3V Logic

MPU Pins

1.8V Logic



JMEDIA

	GND	2	1	GND	
	MIPI_DSI0_L1_P	4	3	MIPI_DSI0_CLK_M	
	MIPI_DSI0_L1_M	6	5	MIPI_DSI0_CLK_P	
	GND	8	7	GND	
	MIPI_DSI0_L0_P	10	9	MIPI_DSI0_L2_M	
	MIPI_DSI0_L0_M	12	11	MIPI_DSI0_L2_P	
	GND	14	13	GND	
GPIO_20	SOC_CAM_MCLK0	16	15	MIPI_DSI0_L3_M	
GPIO_21	SOC_CAM_MCLK1	18	17	MIPI_DSI0_L3_P	
	GND	20	19	GND	
GPIO_29	CCI_I2C_SDA1	22	21	CSI0_C0_LN0_M	
GPIO_30	CCI_I2C_SCL1	24	23	CSI0_B0_LN0_P	
	GND	26	25	GND	
	CSI1_B2_LN3_P	28	27	CSI0_B1_LN1_M	
	CSI1_C2_LN3_M	30	29	CSI0_A1_LN1_P	
	GND	32	31	GND	
	CSI1_C1_LN2_P	34	33	CSI0_A0_CLK_M	
	CSI1_A2_LN2_M	36	35	CSI0_NC_CLK_P	
	GND	38	37	GND	
	CSI1_NC_CLK_P	40	39	CSI0_A2_LN2_M	
	CSI1_A0_CLK_M	42	41	CSI0_C1_LN2_P	
	GND	44	43	GND	
	CSI1_A1_LN1_P	46	45	CSI0_C2_LN3_M	
	CSI1_B1_LN1_M	48	47	CSI0_B2_LN3_P	
	GND	50	49	GND	
	CSI1_B0_LN0_P	52	51	CCI_I2C_SCL0	GPIO_23
	CSI1_C0_LN0_M	54	53	CCI_I2C_SDA0	GPIO_22
	GND	56	55	GND	
	+3V3 OUT	58	57	IN VIN	
	+3V3 OUT	60	59	IN VIN	

! These pins can not be used as regular GPIOs.

Legend :

Power IN Power Input
Ground OUT Power Output

GPIO Digital External
Analog External
Main Part
Secondary Part
Internal Component
Other Pins (Reset, System Control, Debugging)

I2C Default
SPI Default
UART/USART Default
Other SERIAL
Analog Audio Default
PWM/Timer

LED
RGB LED

WARNING Be aware of the pins logic level.
Analog Audio functionality is provided by the PMIC
CIPO/COPI have previously been referred ot as MISO/MOSI



SKU code: ABX00162-ABX00173
Full Pinout - Page 4 of 4
Last update: 2 Sept, 2025

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