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# IQS550/525/512 Capacitive Controller Platform IQS5xx-A000 – Trackpad/Touchscreen Controller

Multiple Channel Projected Capacitive Controller, with Proximity, Touch and Snap

The IQS5xx-A000 is a projected capacitive touch and proximity trackpad/touchscreen controller implementation on the IQS550, IQS525 and IQS512 platforms. The IQS5xx-A000 features best in class sensitivity, signal-to-noise ratio and automatic tuning of electrodes. Low power proximity detection allows extreme low power operation.

#### **Main Features**

- Proximity, touch and snap\* on each channel
- Multi-touch and multi-hover support
- 3584 x 2304 resolution (IQS550)
- I<sup>2</sup>C<sup>TM</sup> communications interface
- ATI: automatic tuning for optimum sensitivity
- Supply Voltage 1.65V to 3.6V
- Proximity low power operation (<10uA)</li>
- Event mode and streaming modes
- Dedicated proximity channel for long range proximity sensing
- Internal voltage regulator and reference capacitor
- On-chip noise detection and suppression

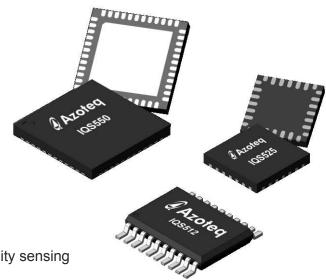
	IQS550	IQS525	IQS512
Maximum channels	150	25	12
Minimum report rate (with dual touch / max channels active)	100Hz	220Hz	400Hz
Resolution (for shown Tx Rx configurations)	3584 x 2304 (15 x 10)	1280 x 768 (6 x 4)	768 x 512 (4 x 3)

#### **Applications**

- Compact Capacitive Keyboards
- Remote Control Trackpads
- Appliances
- Navigation devices
- Kiosks and POS terminals
- E-readers
- Consumer electronics

#### **Available options**

T <sub>A</sub>	QFN(7x7)-48	QFN(4x4)-28	TSSOP-20
-40°C to 85°C	IQS550	IQS525	IQS512







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### IQ Switch<sup>®</sup> ProxSense<sup>®</sup> Series



#### 1 Overview

The IQS550 / IQS525 / IQS512 is a capacitive sensing controller designed for multi-touch applications using projected capacitance touch panels. The device offers high sensitivity proximity/hover (PROX) detection and contact detection (TOUCH) through a selectable number of sensor lines (Rx's and Tx's).

Touch and proximity positions are calculated to provide multiple X-Y coordinates.

The device has an internal voltage regulator and Internal Capacitor Implementation (ICI) to reduce external components. Advanced on-chip signal processing capabilities yields a stable high performance capacitive controller with high sensitivity.

The controller uses the principle of projected capacitance charge transfer on the trackpad. When a conductive object such as a human finger approaches the sense plate it will decrease the detected capacitance. Observing the measured results at various sensing points on the trackpad enables the controller to determine PROX and TOUCH on all channels, and accurately determine the coordinates on the touch area. Multiple touch positions can thus be obtained.

Due to the advanced sensitivity of the device, MULTIPLE non-contact (proximity hover) coordinates can also be obtained. These hover co-ordinates can be used to predict the touch co-ordinate of an approaching user, before the touch is made, allowing innovative user interface options.

Multiple filters are implemented to suppress and detect noise and track slow varying environmental conditions, and avoid effects of possible drift. The Auto Tuning (ATI) allows for the adaptation to a wide range of touch screens without using external components.

An innovative addition, known as a *Snap\** (*Click*), is also available on each channel. This adds another output additional to the PROX and TOUCH of each channel.

The trackpad application firmware on the IQS5xx is very flexible in design, and can incorporate standard touch sensors, trackpad / touchscreen areas (giving XY output data) and conventional snap-dome type buttons, all providing numerous outputs such as Prox, Touch, Snap, Touch Strength and actual finger position even before physical contact, all in one solution.



This datasheet applies to the following **IQS550** version:

Product Number 40 / Project Number 0 / Version Number 55





This datasheet applies to the following **IQS525** version: Product Number 52 / Project Number 0 / Version Number 2





This datasheet applies to the following IQS512 version:



Product Number 53 / Project Number 0 / Version Number 1





#### 2 Packaging and Pin-out

#### 2.1 IQS550 - QFN48

The IQS550 is available in a QFN(7x7)-48 package.

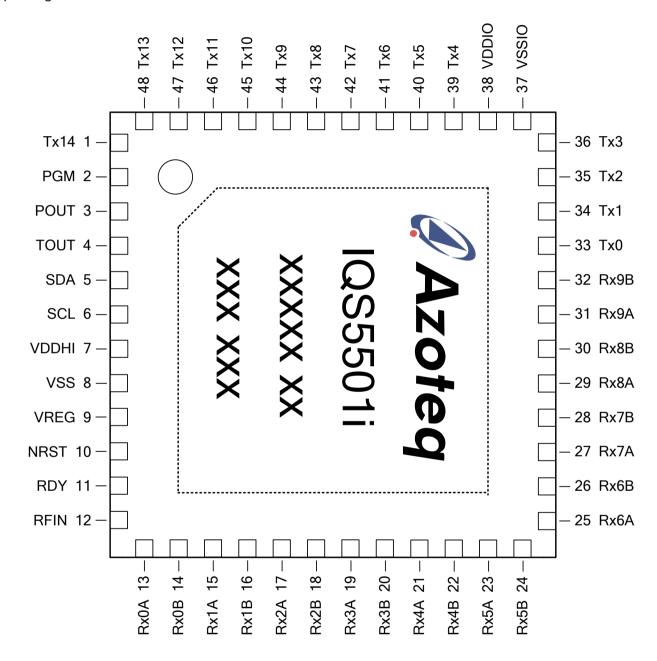


Figure 2.1 QFN Top View





#### Table 2.1 QFN48 Pin-out

Pin	Name	Description	
1	Tx14	Transmitter electrode	
2	PGM	Programming Pin	
3	POUT	Global Prox Output	
4	TOUT	Global Touch Output	
5	SDA	I <sup>2</sup> C Data	
6	SCL	I <sup>2</sup> C Clock	
7	VDDHI	Supply Voltage	
8	VSS	Ground Reference	
9	VREG	Internal Regulator Voltage	
10	NRST	Reset (active LOW)	
11	RDY	I <sup>2</sup> C RDY	
12	RFIN	RF antenna input	
13	Rx0A	Receiver electrode	
14	Rx0B	Default ProxMode electrode	
15	Rx1A	Receiver electrode	
16	Rx1B	Note1	
17	Rx2A	Receiver electrode	
18	Rx2B	Note1	
19	Rx3A	Receiver electrode	
20	Rx3B	Note1	
21	Rx4A	Receiver electrode	
22	Rx4B	Note1	
23	Rx5A	Receiver electrode	
24	Rx5B	Note1	

Pin	Name	Description	
25	Rx6A	Receiver electrode	
26	Rx6B	Note1	
27	Rx7A	Receiver electrode	
28	Rx7B	Note1	
29	Rx8A	Receiver electrode	
30	Rx8B	Note1	
31	Rx9A	Receiver electrode	
32	Rx9B	Note1	
33	Tx0	Transmitter electrode	
34	Tx1	Transmitter electrode	
35	Tx2	Transmitter electrode	
36	Tx3	Transmitter electrode	
37	VSSIO	I/O Ground Reference	
38	VDDIO	I/O Supply Voltage	
39	Tx4	Transmitter electrode	
40	Tx5	Transmitter electrode	
41	Tx6	Transmitter electrode	
42	Tx7	Transmitter electrode	
43	Tx8	Transmitter electrode	
44	Tx9	Transmitter electrode	
45	Tx10	Transmitter electrode	
46	Tx11	Transmitter electrode	
47	Tx12	Transmitter electrode	
48	Tx13	Transmitter electrode	

Note1: Any of these can be configured through I<sup>2</sup>C as the ProxSense® electrode.



#### 2.2 IQS525 - QFN28

The IQS525 is available in a QFN(4x4)-28 package. The production version is shown below.

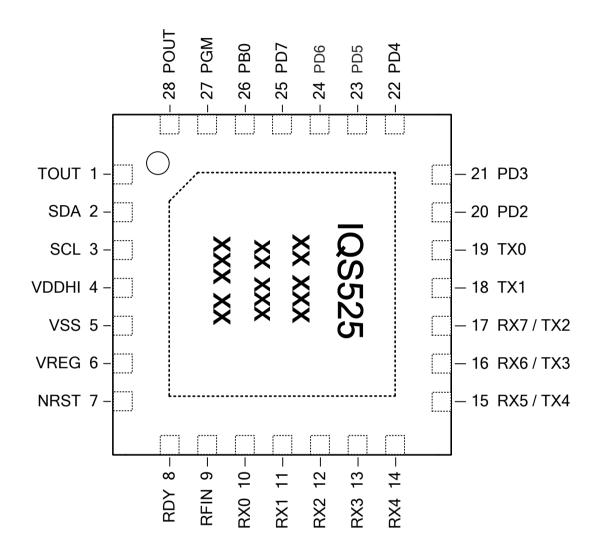


Figure 2.2 QFN Top View





#### Table 2.2 QFN48 Pin-out

Pin	Name	Description	
1	TOUT	Global Touch Output	
2	SDA	I <sup>2</sup> C Data	
3	SCL	I <sup>2</sup> C Clock	
4	VDDHI	Supply Voltage	
5	VSS	Ground Reference	
6	VREG	Internal Regulator Voltage	
7	NRST	Reset (active LOW)	
8	RDY	I <sup>2</sup> C RDY	
9	RFIN	RF antenna input	
10	Rx0	Receiver electrode	
11	Rx1	Receiver electrode	
12	Rx2	Receiver electrode	
13	Rx3	Receiver electrode	
14	Rx4	Receiver electrode	

15	Rx5 / TX4	Receiver / Transmitter electrode
16	Rx6 / TX3	Receiver / Transmitter electrode
17	Rx7 / TX2	Receiver / Transmitter electrode
18	Tx1	Transmitter electrode
19	Tx0	Transmitter electrode
20	PD2	General purpose I/O
21	PD3	General purpose I/O
22	PD4	General purpose I/O
23	PD5	General purpose I/O
24	PD6	General purpose I/O
25	PD7	General purpose I/O
26	PB0	General purpose I/O
27	PGM	Programming Pin
28	POUT	Global Prox Output





#### 2.3 IQS512 - TSSOP20

The IQS512 is currently available in a TSSOP20 package. The production version is shown below.

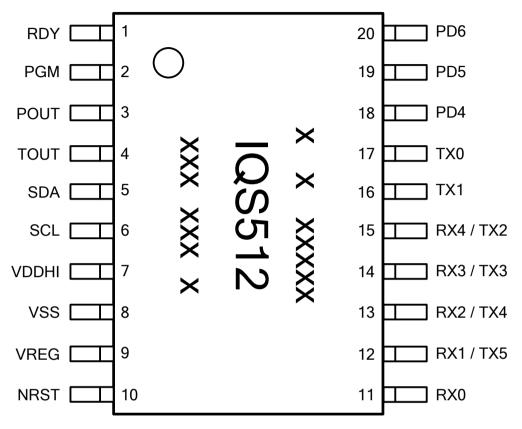


Figure 2.3 TSSOP Top View





#### Table 2.3 TSSOP20 Pin-out

Pin	Name	Description
1	RDY	I <sup>2</sup> C RDY
2	PGM	Programming Pin
3	POUT	Global Prox Output
4	TOUT	Global Touch Output
5	SDA	I <sup>2</sup> C Data
6	SCL	I <sup>2</sup> C Clock
7	VDDHI	Supply Voltage
8	VSS	Ground Reference
9	VREG	Internal Regulator Voltage

	I	
10	NRST	Reset (active LOW)
11	Rx0	Receiver electrode
12	Rx1 / TX5	Receiver / Transmitter electrode
13	Rx2 / Tx4	Receiver / Transmitter electrode
14	Rx3 / Tx3	Receiver / Transmitter electrode
15	Rx4 / Tx2	Receiver / Transmitter electrode
16	Tx1	Transmitter electrode
17	Tx0	Transmitter electrode
18	PD4	General Purpose I/O
19	PD5	General Purpose I/O
20	PD6	General Purpose I/O





### ProxSense® Module

The device contains a ProxSense® module Communication is only done once all these that uses patented technology to provide timeslots are completed. detection of PROX and TOUCH, and calculate X and Y touch and/or proximity coordinates. A combination of hardware and software is used An additional ProxMode charging scheme is to obtain a set of measurements used for calculating the respective outputs.

An additional 'Snap' output is now available which adds further conventional button snap functionality above the trackpad area.

The system can operate in a Normal- or Prox-Mode charging configuration. In both of these a low-power charging scheme can also be implemented.

#### 3.1 Individual Channels

On a trackpad type pattern (typically diamond shape layout), each intersection of an Rx and Tx row/column forms a channel. Each channel has a count value, Long-Term Average, Proximity, Touch and Snap (if enabled) status. The default on the IQS550 device is 15x10 thus giving 150 channels in total.

Any channels not forming part of the trackpad area (see Section 4.3) can be used as separate sensors, and designed with any projected sensor pattern (Rx + Tx) as required by the design.

Each channel is limited in having a count value < 20000. If the ATI setting or hardware samples higher than this, conversion will be stopped, and a value of '0' will be read for that relevant count value.

#### 3.2 Normal Mode Charging

The sensors are scanned one Tx transmitter at a time, until all have completed, with all enabled Rx's charging in each Tx 'time-slot'. This then provides all the sample data for the touch panel, which can be used to obtain Long-term Average values, Prox and Touch status, and finally full XY co-ordinate recommended to route the ProxMode Rx information.

In a 15x10 system (15 Tx and 10 Rx), 15 conversion timeslots occur, with each timeslot

consisting of the acquisition of 10 receiver channels.

#### 3.3 ProxMode Charging

selectable, and aimed at providing long range proximity detection, useful for implementing low-power modes during periods of no user ProxMode activity. The channel configurable, and can function in either self- or projected- capacitive mode. Standard sensor electrode design for self or projected channels must be adhered for optimal proximity sensina.

The system performs a ProxMode channel acquisition (while waiting in a low-power state for the conversion to complete). complete, the data is processed, updating the proximity status. A low duty cycle can then also be selected, further reducing the total current consumption. With the superior sensitivity of the ProxSense® hardware, the system will be fully operational in Normal Mode charging before the user is within operating distance of the sensors.

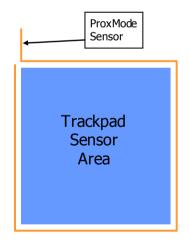


Figure 3.1 **ProxMode Layout** Suggestion

If a trackpad is implemented on which a ProxMode is to be designed. channel around the perimeter of the trackpad The channel is then configured in projected capacitive sensing mode, and all the Tx's on the trackpad sensors are enabled.





trackpad area. between this Rx and the trackpad channels power). If no activity occurs for ModeTime, also improves the proximity sensing.

#### 3.4 Automatic Mode Switching

With this enabled, automatic interaction and switching between Normal- and ProxMode charging is achieved. For the automatic mode (and also low-power charging) a selectable ModeTime is used.

After being correctly configured, the system will operate in ProxMode, until a proximity event is sensed on the ProxMode channel. At this point the system will switch to Normal Mode charging, ready to sense interaction with the trackpad channels.

The system will revert back to ProxMode charging if no prox, touch or snap condition is found on the Normal Mode channels for a period of *ModeTime*.

When in ProxMode. а Normal Mode conversion will automatically take place roughly every 4s, but no communication for this cycle will be presented. This cycle is simply to keep the Long-Term Average values of the Normal Mode channels up to date.

#### 3.5 Low Power Charging

By enabling low-power, the device will add a sleep between conversions. If no prox, touch or snap is sensed for *ModeTime*, then the system will go into the low duty-cycle charging As soon as a channel output is sensed, the system will resume full-speed Introducing this low-power duty cycle into the system naturally decreases total power consumption of the device, dependant on the selected LPTime.

#### 3.5.1 Low-Power with Automatic Mode ProxMode, and will charge at full-speed. Switching

If it is possible to have a ProxMode sensor, the best is to use the system in automatic mode switching, combined with low-power This will give by far the lowest power consumption, and works as follows:

The system charges in ProxMode until a PROX event occurs, and since automatic

This gives a good proximity sensor in the mode is enabled, it will switch to Normal Mode Adding a ground track charging which occurs at full-speed (no lowthen the system will revert to ProxMode charging. Here it will continue at full-speed for a further *ModeTime*, and after this, low-power charging will take place on the ProxMode channel.

> This gives the lowest power consumption, combined with a possibility of good range proximity (sensor and system dependent) which helps with the wake-up response time of the system.

#### 3.5.2 Low-Power in Normal Mode

If automatic mode is NOT selected, the system will work as follows in Normal Mode (possibly the hardware does not allow for a ProxMode electrode):

After *ModeTime* of no prox, touch or snap, the system will stay in Normal Mode, but all channels will with a low-power duty cycle. If an event is sensed on a Normal Mode channel, full-speed charging will commence immediately to provide fast response. will not provide the same low power consumption as found in ProxMode, since more channels are usually processed in Normal Mode. thus increasing the consumption (longer/more processing means higher ratio of high current operation time versus sleep time).

#### 3.5.3 Low-Power in ProxMode

If automatic mode is NOT selected, and the system is in ProxMode charging:

After a period of *ModeTime* of no PROX on the ProxMode sensor, the ProxMode channel will charge in the low-power mode. If a PROX is sensed on this channel, it will stay in

#### 3.6 Data Report Rate

The report rate of the device depends on the charge transfer frequency, number of enabled channels, and the count value of the channels. The length of communications initiated by the master device will also affect the report rate. There is a maximum rate governed by the





can be decreased if the other factors extend event, a filter halt scheme is implemented on the cycle.

The frequency of the transfers can be adjusted. An optimal transfer frequency must be selected for a specific touch panel application by choosing the optimal setting.

High count values will give good resolution and proximity hover performance, but could decrease the report rate.

For an indication of the expected report rate. please refer to Table 7.7.

#### 3.7 Count Value Filter

#### 3.7.1 Filter in Normal Mode

To improve hover reliability, a count value filter is implemented in normal mode. Since this would greatly reduce the response rate for normal touch operation, it is only active when no touch or snap output is sensed. When these occur the filter is bypassed and fast response is achieved. This filter can also be disabled and adjusted.

#### 3.7.2 Filter in ProxMode

For the ProxMode channel, a count value filter is implemented. This allows the user to increase the sensitivity of the ProxMode electrode drastically to obtain good proximity distance, whilst the filter retains the stability of the count values and thus the PROX output. The filter damping factor can be adjusted, and the filter can also be totally disabled.

#### 3.8 Environmental Drift

The Long-Term Average (LTA) can be seen as the baseline or reference value.

#### 3.8.1 Long-Term Average Filter

The Long-Term Average filter is calculated from the sample count value of each channel. allows the device to adapt environmental (slow moving) drift. To force an update, a reseed command can be executed.

#### 3.8.2 Filter Halt

time taken to process the data, but the rate do not adapt during a prox, touch or snap the device. The designer can choose between filter halt times ranging from 0.5 to 127 seconds, in multiples of 500ms. Also 'Always Halt' can be selected (value = 255). Once this filter halt time has elapsed, a recalibration (reseed) is executed, resetting all outputs and incorporating the current environment into the new baseline.

#### 3.9 Auto Tuning (ATI)

ATI is a sophisticated technology implemented in the new ProxSense® devices to allow optimal performance of the devices for a wide range of sensing electrode capacitances, without modification to external components. The ATI allows the tuning of two Multiplier parameters, ATI and Compensation, to adjust the sample value for an attached sensing electrode.

ATI allows the designer to optimise a specific design by adjusting the sensitivity and stability through the adjustment of the ATI parameters.

With a selected ATI C value, the ATI Compensation can then be automatically configured for an adjustable channel target sample value by means of an automated ATI function.

The device requires that for optimal performance on the trackpad, all these channels must be configured to have similar sample values.

Different sets of settings exist for trackpad. non-trackpad and the ProxMode channel.

#### 3.10 Snap\* (Metal-Dome click)

When adding a metal snap-dome button as the overlay to the trackpad pattern, an additional 'Snap' function is available. device is able to distinguish between a normal 'touch' on the overlay and an actual button 'snap', which depresses the metal dome onto the Rx/Tx pattern. This output is referred to as a snap. The design must be configured so that a snap on the metal dome will result in a To ensure that the Long-Term Average filters channels' sample value falling well below the



Long-Term Average value for that channel.

A few suggestions are:

- Place the snap-dome directly above a channel (thus exactly on the Rx-Tx junction)
- Alternatively place the snap-dome in the centre of the diamond pattern, and add a round pad of the second sensor inside the diamond.
- The snap-dome must consist of the standard metal dome or carbon circle pattern (or similar conductive material) on the inside of the dome.
- This conductive dome must be of adequate size to provide good count value deviation below the Long-Term Average of the channel on a snap.
- not be too big relative to the pitch of the Rx/Tx sensors, so as to not block the field lines for the trackpad sensing.
- No electrical connection between the snap-dome and the Rx-Tx must be Usually PCB solder-mask is adequate. Optimally the sensors are covered by solder-mask, with the snapdome directly above.
- The snap-dome overlay must not have varying air-gaps between itself and the Thus having the overlay securely fastened to the PCB is ideal. A variable air-gap causes sporadic sensing, and gives unreliable data.

If required, the function can be enabled, and the snap bits are then available to the user, similar to the prox and touch status bits. The Long-Term Average filter halt is also implemented on snap outputs.

With the high level of sensitivity found on the device, a touch- / track-pad can now be projected through conventional keys, providing full XY functionality behind these without requiring additional real-estate or sense ICs.

For more information, refer to the Application Note: AZD068 – Trackpad Design Guide.

#### 3.11 Proximity Sensitivity

The proximity threshold of the channels is calculated as a delta value of the count value relative to the Long-Term Average value. A PROX status is detected when the count value changes by more than the selected delta. Any 8 bit value can be used as the proximity threshold delta value.

A different threshold is available for the trackpad, non-trackpad and the ProxMode channels.

Note: For the trackpad channels (projected capacitive) the samples will increase with user interaction, thus the threshold is this value ABOVE the Long-Term Average.

However for the ProxMode channel, if self capacitive mode is selected, the samples will decrease during user interaction, thus the threshold is this value BELOW the Long-Term Average.

The conductive dome must however If ProxMode Reverse sensing (ControlSettings1 byte) is enabled, the proximity output will trigger on a positive or negative change. It has been found that for certain battery applications, even though projected capacitance is selected, a self-capacitive effect can occur.

#### 3.12 Touch Sensitivity

The touch sensitivity of the channels is a user defined threshold calculated as a ratio of count value to the Long-Term Average for each channel. Note that a user touching the sensor will cause the count value to increase. A smaller fraction will thus be a more sensitive threshold (for example 1/64 is more sensitive than 1/16)

The touch threshold for a specific channel is calculated as follows:

### Threshold = LTA x (1 + Multiplier / $2^{SHIFTER}$ )

where the MULTIPLIER and SHIFTER values can be adjusted.

If the count value increases with more than this threshold value, then a touch condition is true.

A touch is NOT calculated for the ProxMode channel, but two sets of thresholds are available for the trackpad and non-trackpad channels.





#### 3.13 Snap Sensitivity

a snap is performed, a self capacitive effect is These a normal touch release, the hardware must be hardest to least hard touch/prox. value can be selected for this delta.

When a user touches the key, the samples on normal touch and prox output will trigger. When the user pushes the button down (snap), the samples will decrease (removing the prox and touch outputs) below the Long-Term Average value, and a snap output can be observed.

One global Snap threshold is implemented.

#### 3.14 Output Debounce

All the channel outputs (proximity, touch and snap) are debounced according to the selectable debounce values. The default debounce values are shown in the table, note that a debounce value = 1 means that two samples satisfying the condition must be met before the output is activated. A debounce value of 0 thus means no debouncing takes place. The default touch debounce setting is set to no debouncing. This is due to the fact that with a 15x10 sensor, debouncing adds too much delay, and fast movements on the touch panel cannot be debounced fast enough to provide reliable XY output data. With the advanced sensitivity of the sensors, a touch is regarded as a large deviation this does not pose any problems.

Table 3.1 **Debounce values** 

	Set	Clear
Proximity	4	4
Touch	0	0
Snap	1	1

#### 3.15 Touch and Proximity XY Data

The Snap threshold is a delta value BELOW Five XY co-ordinates are available to the the Long-Term Average of the channel. When master (*Note*: The IQS512 is limited to 2). the are 5 'hardest/biggest' observed, and the sample value will decrease. touches/proximity points sensed on the sensor To be able to distinguish between a snap, and panel. The XY data is sent out in order, from designed so that a snap forces the samples however tracks each specific XY position from below the Long-Term Average value. A 16-bit cycle to cycle (since they will move in position relative to their touch strength) and attaches the relating ID tag with each co-ordinate.

that specific channel will increase and a 5 unique ID tags are available for TOUCH coordinates, namely values of 1-5, and 5 ID's for HOVER co-ordinates, namely 129 - 133.

> 256 steps are implemented between the relative Rx's and also between the relative Tx's, giving x coordinates that range from 0 to (256 x (*TrackpadRxs*-1)).

> The Y coordinates will have an output range from 0 to (256 x (TrackpadTxs-1)). Thus in a 15x10 system: (0<y<3584) and (0<x<2304).

> It is not necessary to read the proximity, touch or snap status data to obtain a global picture of the touch panel status. The XY data stream contains all information required during normal operation.

> The XY data stream is lead by an information byte showing certain status bits, as well as how many active co-ordinates are currently available (the number of touches + hovers).

> For each co-ordinate, the ID tag is sent first, followed by an X co-ordinate, a Y co-ordinate, and finally the touch strength. All of these are 2 bytes each.

> A total of 5 (2 on IQS512) co-ordinates are always available to be read, but if the information byte indicates fewer co-ordinates are active, the master is allowed to stop reading after the relative co-ordinate. A global snap indication bit is also available in the information byte and will indicate when the individual status bits must be read.

> On the IQS525 and IQS512, an option exists to force the sensing to calculate a single XY output, based on all the sensing data on the trackpad channels. This can be useful for certain applications with non-ideal overlays.



Incorrect multiple points could be detected touch points, since the hover operates closer due to the overlay, where a single output to the system's noise floor. using all trackpad data would still give a reliable position.

#### 3.16 Position Tracking

Position filtering is performed on-chip, and is configurable through the I<sup>2</sup>C interface. filtering to be possible, position tracking is required.

Each calculated XY co-ordinate must be matched with the previous co-ordinates to be able to identify a specific point. The coordinates are identified by an identification value (ID) that is provided along with each XY extra effects in applications. data packet. This ID allows the master to applications differentiate between Touch and Hover coordinates, and also track specific points.

#### 3.17 Touch Co-ordinate Filtering

Selecting between dynamic, static or no filtering is possible.

#### 3.17.1 Dynamic Filter

Relative to the speed of movement of a coordinate, a dynamic filter is implemented on 3.21 Physical Layout the touch co-ordinates.

movement of the XY co-ordinate. When fast can be chosen to Similarly when a co-ordinate is stationary or output data. moving at a slower speed, then more filtered XY co-ordinates are obtained.

#### 3.17.2 Static Filter

Co-ordinates filtered with fixed configurable damping factor are obtained when using the static filter. It is recommended that the dynamic filter be used due to the advantages of a dynamically changing damping value.

#### 3.18 Hover / Prox Co-ordinate **Filtering**

A static filter is implemented on the hover coordinates, and can be adjusted independently from the touch co-ordinate filter. The hover points are usually filtered much more than the

#### 3.19 Position adjustment / **Calibration**

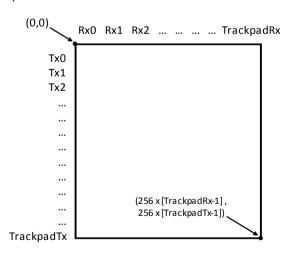
No position calibration is required. position data starts at the centre of the first Rx/Tx electrode, and ends at the centre of the last Rx/Tx electrode.

#### 3.20 Touch Strength

This value indicates the strength of the touch/proximity with the touch screen. touch strength information can be used to add Two possible would be broadening paintbrush width while drawing on the touch screen, or sensing presses of the finger while moving across a touch panel, without lifting your finger. The calculated value of the touch strength can be seen as providing 'Z' direction data.

This strength value varies according to the sensitivity of the sensors.

The Rx and Tx channels must be connected The filter dynamically adjusts the amount of sequentially for the trackpad channels, as filtering (damping factor) relative to the shown in Figure 3.2. To assist routing, they be ascending response is required, less filtering is done. descending, this will just invert the X or Y



Hardware Rx/Tx Setup Figure 3.2





#### **Additional Features**

#### 4.1 Event Mode Communication

The device can be set up to bypass the communication window when no activity is sensed. This could be enabled if a master does not want to be unnecessarily interrupted every charging cvcle. communication will resume (RDY will indicate available data) if a proximity, touch or snap is sensed. It is recommended that the RDY be placed on an interrupt-on-pin-change input on 4.2.1 Design Guidelines the master.

As soon as the active output is no longer sensed, one communication cycle will still be available to be able to read that this event has ended, and then the communication will again cease until further interaction with the sensors are observed.

Using the Event Mode will typically work as follows:

The master sends a command to enable event The device then continually does mode. without conversions interaction (communication) with the master, until a proximity, touch or snap event occurs, which is most likely the first time that the master will be interested in the data. The master reads data during the communication windows, until the event is over, and then the communication windows will again be bypassed.

If however the master would like to force communication session, it must perform a single byte read from the device at any time (obviously without the need for RDY to go HIGH, which it won't since communication is skipped). The master will read one byte with a value of 0xA3, and then the master gives an I<sup>2</sup>C STOP. This shows that a request for a communication session is successful. when the next set of data is ready, temporary communication session will be forced (and RDY will be set as usual to indicate this). This will however not disable Event Mode, and if this is required, it must be disabled during this single temporary window.

The different events to trigger the Event Mode, namely ProxMode Proximity, Proximity, Touch

or Snap, can be configured. For example all events except Touch can be disabled, and then communication will only be available when a touch output is detected.

#### 4.2 RF Immunity

The IQS5xx has immunity to high power RF noise. In this section general design guidelines will be given to improve noise immunity and the noise detection function is explained.

To improve the RF immunity extra decoupling capacitors are suggested on V<sub>REG</sub> and V<sub>DDHI</sub>.

Place a 100pF in parallel with the 1uF ceramic on  $V_{REG}$ . Place a 1uF ceramic on  $V_{DDHI}$ . All decoupling capacitors should be placed as close as possible to the  $V_{DDHI}$  and  $V_{RFG}$  pads.

PCB ground planes also improve noise immunity.

#### 4.2.2 RF detection

In cases of extreme RF interference, on-chip RF detection is provided. By connecting a suitable RF antenna to the RF input pin, it improves detection of RF noise. The RF noise is identified on-chip, and suitable steps are taken to block the corrupt samples from influencing the output data. In standard designs this will not be necessary, since the on-chip sensing has a good immunity to noise interference.

#### 4.3 Additional Non-Trackpad Channels

If there is a requirement for standard projected sensors that do no form part of the trackpad area, then this can be configured. trackpad sensors can be reduced by defining the *TrackpadRx* and *TrackpadTx* parameters. These define where the trackpad ends. Any Rx/Tx channels remaining can be used as standard capacitive sensing buttons/sensors, and only proximity, touch and snap data is processed on these channels, no XY data. An example is shown in Figure 4.1. Here the trackpad area will be 8x7, and an additional 14 sensors are available outside of this area. Clearly the following must be true:



#### TrackpadRxs ≤ TotalRxs TrackpadTxs ≤ TotalTxs

Not all of the channels inside the blocks need to be used, these can also individually be disabled, see Section 4.4.

Separate proximity and touch thresholds, as well as separate ATI settings are implemented on these non-trackpad channels.

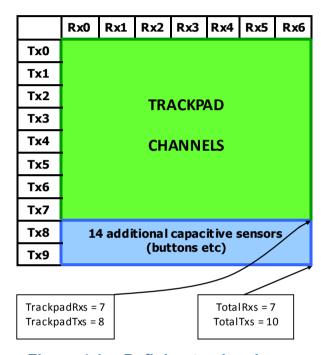


Figure 4.1 Defining trackpad area

### 4.4 Active Channels / Disabling Channels

If irregular shape trackpads are required, or certain channels are not required, they can individually be disabled. These channels are then skipped, and no calculation is performed on these channels.

This is useful for example when a round cases be trackpad is used. Here the trackpad area is performance. defined by *TrackpadTx* and *TrackpadRx*, which is a square selection of channels, but some corner channels do not exist due to the

round structure. To allow accurate XY coordinates to be calculated, the non-existing corner channels can be disabled. It is recommended that all non-existent (not part of physical sensor layout) channels falling within the selected *TotalRx* and *TotalTx* block should be disabled.

Note that non-linear edge effects can still take place on such an irregular shaped trackpad, and if the design allows, it is better to keep a square sensor, and process the XY values to only output the required shapes' co-ordinates.

#### 4.5 POUT / TOUT

The POUT I/O is HIGH if there is proximity on any of the channels (including ProxMode channel). This can for example be used to control backlighting in the design.

The TOUT output indicates a touch or snap condition on any of the Normal Mode channels.

#### 4.6 Sleep

A constant low-power sleep state can be added to each complete system cycle. This will clearly reduce the average operating current of the device, at the expense of reduction in response rate.

When activated, this is permanently added in **only the Normal Mode** charging, irrespective of the low-power setting/state.

In current critical applications, where a response rate slower that the full-speed rate is acceptable, this further reduction in power consumption can be implemented.

The length of the time that the system is in this sleep state each cycle is set by changing the SleepTime value. This value will in most cases be  $\leq 20ms$  to still obtain good performance.





#### 5 Communication

#### 5.1 I<sup>2</sup>C

The device can communicate in I<sup>2</sup>C using the standard communication protocol. An additional RDY signal is added which indicates when the communication window is available, it is thus optimal for response rate to use the RDY as a communication trigger, but polling is also available as a less attractive option. Designing the RDY to connect to an interrupt-on-change input is recommended for easier implementation and optimal response time.

The first communication window is available before the device performs any sensing or calculations, to allow initial configuration to take place.

Standard I<sup>2</sup>C clock stretching can occur, so monitoring the availability of the SCL is required, as per usual I<sup>2</sup>C protocol.

#### 5.1.1 Protocol

The I<sup>2</sup>C currently employs an 'address-command' type structure instead of a memory map. What this means is that data bytes cannot be individually addressed, but can be obtained by configuring a relevant address-command on the device to specify which blocks of data to read or write. Specific data is thus grouped together, and identified / accessed by means of the 'address-command' relating to the specific group.

Table 5.1 I<sup>2</sup>C Address-Command Structure

Command Value	Command Description	Read/ Write
0x00	Version Info	R
0x01*	XY Data (default)	R*
0x02	Proximity Status	R
0x03	Touch Status	

0x04	Count Values	R
0x05	Long-Term Averages	R
0x06	ATI Compensation	R/W
0x07	Port Control	R/W
0x08	Snap Status	R
0x10	Control Settings	R/W
0x11	Threshold Settings	R/W
0x12	ATI Settings	R/W
0x13	Filter Settings	R/W
0x14	Timing Settings	R/W
0x15	Channel Setup	R/W
0x16	Hardware Config Settings	R/W
0x17	Active Channels	R/W
0x18	Debounce Settings	R/W
0x20	PM Proximity Status	R
0x21	PM Count Values	R
0x22	PM Long-Term Averages	R
0x23	PM ATI Compensation	R/W
0x24	PM ATI Settings	R/W

For example, to read out the proximity status bytes, the following must be done:

START -->
CONTROL BYTE(Write) -->
0x02 (proximity read address-command) -->
REPEATED-START -->
CONTROL BYTE(Read) -->
ProxByte[0] ->

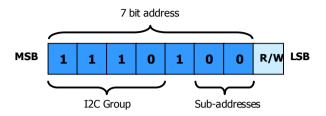
ProxByte[TotalTx-1] -->
STOP





#### 5.2 Control Byte

The 7-bit device address is '1110100'. Currently the sub-address is fixed at '00'. An option does exist to change the device address if needed.



I<sup>2</sup>C Control Byte Figure 5.1

#### 5.3 I<sup>2</sup>C Read

To read from the device a current address read can be performed. This assumes that the address-command is already setup as desired.



Figure 5.2 **Current Address Read** 

the address-command must first be specified, then a random read must be and then a repeated start is used to initiate the of all required data. READ section.

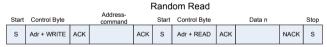


Figure 5.3 **Random Read** 

#### 5.4 I<sup>2</sup>C Write

To write settings to the device a Data Write is Here the Address-Command is always required, followed by the relevant data bytes to write to the device.



Figure 5.4 **Data Write** 

#### 5.5 End of Communication Session / Window

Similar to other Azotea I<sup>2</sup>C devices, to end the communication session, an I<sup>2</sup>C STOP is given. When sending numerous read and write commands in one communication cycle. a repeated start command must be used to string them together (since a STOP will jump out of the communication window, which is not desired).

The STOP ends the communication, RDY goes LOW, and the device will return to process a new set of data. Once this is obtained, the communication window will again become available (RDY set HIGH).

#### 5.6 Address-Command **Description**

In the address-commands, the length of the available data is often relative to the total Rx and Tx channels configured on the device. To indicate the length of these commands, the terms TotalTxs and TotalRxs will be used.

Most of the commands allow the master to Read/Write certain data. However in normal performed. In this case a WRITE is initially operation initially only the XY Data Read is performed to setup the address-command, recommended, since it gives a good summary The other addresscommands should only be used setup/configuration, or when needed.

> Note: For the IQS525 and IQS512 platforms often the upper byte of 2byte data will never be used. However these upper bytes are still present to maintain compatibility.

#### 5.6.1 Version Information Read (0x00)

Here device version information can be A Product Number (2 bytes), obtained. Project Number (2 bytes), Major Release Number (1 byte) and Minor Release / Build Number (1 byte) are available, followed by a Hardware ID, and Hardware Revision (2 bytes each). A total of 10 bytes are thus available.

#### 5.6.2 XY Data Read (0x01)

The **default** address-command at the start of each communication window is set to the XY





data read address-command.

This means The XY data consists of the status byte and that if a Current Address Read is performed at the following repeated sets of data bytes, the start of the communications window relative to the number of multi-touch points without having set the address-command used. Currently 5 points are implemented, before this, then the XY Data will be obtained. and thus a total of (7x5 + 1) 36 bytes are available to be read in XY Data Read mode.

**Table 5.2 XY Data** 

Byte No.	Data	Description
1	XY Info Byte	Status bytes, and indicates the total number of active co-ordinates
2 / 9 / 16	ID Tag	Identify a specific co-ordinate
3 / 10 / 17	Xpos – High Byte	X Position
4 / 11 / 18	Xpos – Low Byte	
5 / 12 / 19	Ypos – High Byte	Y Position
6 / 13 / 20	Ypos – Low Byte	
7 / 14 / 21	Touch Strength – High Byte	Touch Strength – indicates the 'hardness' of this
8 / 15 / 22	Touch Strength – Low Byte	touch

	XYInfoByte							
Bit	7 6 5 4 3 2							0
Name	SHOW_ RESET	MODE_ INDICATOR	NOISE_STATUS	LP_STATUS	SNAP_ OUTPUT	NO_C	F_FIN	GERS

Bit 7: SHOW RESET: This bit can be monitored to determine whether a reset occurred on the device after the ACK RESET was last sent. The value of SHOW RESET can be set to '0' by writing a '1' in the ACK\_RESET bit.

0 = No reset has occurred since last cleared by ACK RESET command

1 = Reset has occurred

Bit 6: MODE INDICATOR: This shows in which mode the device currently is (only required in Auto Mode)

0 = Normal Mode Charging

1 = ProxMode Charging

Bit 5: **NOISE\_STATUS:** This bit indicates the presence of RF noise interference.

0 = No noise detected

1 = Noise detected (noise affected outputs are not used on-chip)





Bit 4: LP STATUS: This bit indicates if the charging is at full-speed, or low-power

0 = full speed charging1 = low-power charging

Bit 3: SNAP OUTPUT: This bit indicates if any snap outputs are active

0 = No active snap outputs1 = At least one snap output

Bit 2-0 NO OF FINGERS: Indicates how many active XY data points (number of

touches + hovers) there currently are.

#### 5.6.3 Proximity Status Read (0x02)

The proximity of each individual channel can be retrieved from the IC. After writing the 'Proximity Status' address-command, a read can be performed to obtain the proximity

bytes. The proximity of each channel found for a corresponding Tx can be obtained from two bytes. This gives a total number of proximity bytes of 2 x *TotalTxs*.

Table 5.3 Proximity Status Bytes

Byte	Byte Data Description	
1	Prox[Tx0] – High Byte	Proximity bits (Rx0 = bit0, Rx1 = bit1)
2	Prox[Tx0] – Low Byte	
3	Prox[Tx1] – High Byte	Proximity bits (Rx0 = bit0, Rx1 = bit1)
4	Prox[Tx1] – Low Byte	
	•	
(2 x TotalTxs) - 1	Prox[Last Tx] - High Byte	Proximity bits (Rx0 = bit0, Rx1 = bit1)
(2 x TotalTxs)	Prox[Last Tx] - Low Byte	

#### 5.6.4 Touch Status Read (0x03)

The touch status of each individual channel can also be retrieved from the IC, exactly the same as the proximity status. After writing the 'Touch Status' address-command, a read can be performed to obtain the touch bytes.

The touch of each channel found for a corresponding Tx can be obtained from two bytes. This gives a total number of touch bytes of 2 x *TotalTxs*.





Table 5.4 **Touch Status Bytes** 

Byte	Data	Description
1	Touch[Tx0] – High Byte	Touch bits (Rx0 = bit0, Rx1 = bit1)
2	Touch [Tx0] - Low Byte	
3	Touch [Tx1] - High Byte	Touch bits (Rx0 = bit0, Rx1 = bit1)
4	Touch [Tx1] - Low Byte	
:	:	:
(2 x TotalTxs) - 1 Touch [Last Tx] - High Byte		Touch bits (Rx0 = bit0, Rx1 = bit1)
(2 x TotalTxs)	Touch [Last Tx] – Low Byte	

#### **5.6.5** Count Data Read (0x04)

When this address-command is setup, all the count values can be read from the device. The count values are written out, high byte Each consists of 2 bytes, giving a total of (2 x first, row by row. Thus all of the first row's most likely be a large amount of data, and to and so on. obtain better response rate, it is NOT recommended to read this continuously.

Normally this is only read while setting up the ATI parameters.

TotalTxs x TotalRxs) bytes available. This will (Tx) count values are sent, then the second,

Table 5.5 **Count Value Bytes** 

Byte	Data	Description
1	Count Value[0][0] – High Byte	count @ first row, and first
2	Count Value[0][0] - Low Byte	column (thus top left)
3	Count Value[0][1] – High Byte	count @ first row, and 2nd column
4	Count Value[0][1] - Low Byte	Column
		·
·	•	
(2 x TotalTxs x TotalRxs) - 1	Count Value[Last Tx][Last Rx] – High Byte	count @ last row, and last column (thus bottom right)
2 x TotalTxs x TotalRxs	Count Value[Last Tx][Last Rx] – Low Byte	





#### 5.6.6 Long-Term Average Data Read Average consists of 2 bytes, giving a total of (2 x TotalTxs x TotalRxs) bytes available. (0x05)

All the Long-Term Averages are available to The Long-Term Averages are read in the be read from the device. Each Long-Term same order as the count values, from top left (row 0 and column 0), row by.

Table 5.6 **Long-Term Average Bytes** 

Byte	Data	Description
1	LTA[0][0] – High Byte	LTA @ first row, and first column (thus top left)
2	LTA [0][0] - Low Byte	column (mus top leit)
3	LTA [0][1] – High Byte	LTA @ first row, and 2nd column
4	LTA [0][1] - Low Byte	Column
:	i.	:
(2 x TotalTxs x TotalRxs) - 1	LTA[Last Tx][Last Rx] – High Byte	LTA @ last row, and last column (thus bottom right)
2 x TotalTxs x TotalRxs	LTA[Last Tx][Last Rx] – Low Byte	

#### 5.6.7 ATI Compensation Read & Write the address-command, two bytes must be (0x06)

read/write each channels Compensation setting, a read/write must be performed after setting up the corresponding address-command to the IC. Each channels' ATI Compensation consists of 1 byte, giving a total of (TotalTxs x TotalRxs) bytes to read/write to/from the device. Again the channels are read/written from Tx0, Rx0, row by row down to the bottom right corner. This is however a lengthy process of configuring the ATI Compensation and the Auto-ATI is recommended (using a target value and allowing the device to automatically setup the ATI Compensation to obtain that target).

#### **5.6.8 Port Control (0x07)**

Tx's not used for sensing can be used as general purpose outputs. Clearly they are to being switched during communication window. The 15 Tx's map to I/Os as shown in the table below. After setting

written, firstly PORTB, followed by PORTD. Not all of these are available on the smaller platforms.

**Outputs** Table 5.7

Tx	Port	Tx	Port
Tx0	D0	Tx8	В0
Tx1	D1	Tx9	B1
Tx2	D2	Tx10	B2
Tx3	D3	Tx11	В3
Tx4	D4	Tx12	B4
Tx5	D5	Tx13	B5
Tx6	D6	Tx14	B6
Tx7	D7	~	~





#### 5.6.9 Snap Status Read (0x08)

The snap status of each individual channel can also be retrieved (if Snap functionality is

enabled), exactly the same as the proximity and touch status bits.

**Table 5.8** Snap Status Bytes

Byte	Data	Description
1	Snap[Tx0] – High Byte	Snap bits (Rx0 = bit0, Rx1 = bit1)
2	Snap [Tx0] - Low Byte	
3	Snap [Tx1] – High Byte	Snap bits (Rx0 = bit0, Rx1 = bit1)
4	Snap [Tx1] - Low Byte	
:	:	:
(2 x TotalTxs) - 1	Snap [Last Tx] - High Byte	Snap bits (Rx0 = bit0, Rx1 = bit1)
(2 x TotalTxs)	Snap [Last Tx] - Low Byte	

#### 5.6.10 Control Settings (0x10)

To send the data relating to certain control commands, the Control Settings address-

command must be used. Two bytes follow the address-command, namely *ControlSettings0* and *ControlSettings1*.

	ControlSettings0								
Bit	7	6	5	4	3	2	1	0	
Name	ACK_ RESET	AUTO_ MODES	SINGLE _XY	PM_ RESEED	MODE_ SELECT	AUTO_ ATI	TRACKPAD_ RESEED	EVENT _MODE	
Default	0	0	0	0	0	0	0	0	

Bit 7: ACK\_RESET: Acknowledge 'Indicate Reset' bit

0 = Nothing

1 = Clears the flag **SHOW\_RESET** in the **XY Info Byte** 

This is used to be able to notice if an unexpected reset has occurred. After setting up the device, the master can clear the SHOW\_RESET flag to confirm the device is correctly setup. If the SHOW\_RESET bit ever becomes set, the master will know a reset has occurred, which would mean the device must again be setup with the necessary parameters.





Bit 6: AUTO\_MODES: Automatic mode switching between ProxMode and Normal Charging Mode

0 = Mode is decided by the MODE SELECT bit

1 = Mode is automatically controlled

Bit 5: SINGLE\_XY: Switching between multi-touch output to single XY output

0 = Multi-touch detection done on trackpad area

1 = Single touch output from all trackpad data (only on IQS525/512)

Bit 4: PM\_RESEED: Reseed the ProxMode (PM) channel

0 = Do not reseed Long-Term Average

1 = Reseed Long-Term Average with current environment

*Note*: This only executes (once) after the communication window is completed.

Bit 3: MODE\_SELECT: Select charging mode (if AUTO\_MODES is not set)

0 = Normal Mode channels charging

1 = ProxMode channel charging

Bit 2: AUTO\_ATI: Begin Automatic ATI Compensation routine

0 = No nothing

1 = Begin Auto-ATI routine (affected channels depending on current mode)

The AUTO-ATI bit must be sent ONCE to begin the AUTO-ATI routine. The ATI Compensation will be setup so that each target is close to the respective ATI Target value selected. The bit clears automatically on chip. This bit will then configure the ATI compensation relative to the current mode selected; for example if the system is in ProxMode, then the PM ATI Target will be used, and the ProxMode channel will be configured, similarly the normal mode channels will be configured if this is the current mode.

*Note*: This routine only executes after the communication window is completed. Also the following communication cycle will occur after the routine is completed.

Bit 1: TRACKPAD RESEED: Reseed all the Normal Mode channels

0 = No not reseed Long-Term Average

1 = Reseed Long-Term Average with current environment

*Note:* The RESEED bit for both the trackpad and the ProxMode must be sent only ONCE to reseed the Long-Term Averages, the bit clears automatically on chip. This reseed only executes after the communication window is completed.

Bit 0: **EVENT\_MODE:** Skip communication when no user activity is present

0 = Normal communication / each cycle

1 = Communication aborted until selectable activity (prox/touch/snap) is detected, or the master forces communication





	ControlSettings1								
Bit	7	6	5	4	3	2	1	0	
Name	DIS_ PROX_ EVENT	DIS_ TOUCH_ EVENT	DIS_ SNAP_ EVENT	DIS_ PMPROX_ EVENT	REVERSE _EN	SLEEP_ EN	LOW_ POWER	SNAP_ EN	
Default	0	0	0	0	0	0	0	0	

Bit 7: DIS PROX EVENT: Remove proximity from Event Mode events

0 = Proximity on Normal Mode channels will trigger the Event Mode

1 = Proximity cannot trigger the Event Mode

Bit 6: DIS TOUCH EVENT: Remove touch from Event Mode events

0 = Touch will trigger the Event Mode

1 = Touch cannot trigger the Event Mode

Bit 5: DIS\_SNAP\_EVENT: Remove snap from Event Mode events

0 = Snap will trigger the Event Mode

1 = Snap cannot trigger the Event Mode

Bit 4: DIS\_PMPROX\_EVENT: Remove proximity from Event Mode events

0 = Proximity on ProxMode channel will trigger the Event Mode

1 = Proximity cannot trigger the Event Mode

Bit 3: REVESE EN: Sense proximity changes in BOTH directions

0 = Allow only positive deltas in Projected-, and negative deltas in selfcapacitive setting to trigger proximity output

1 = Positive and negative deltas can trigger a proximity event

Bit 2: SLEEP\_EN: Add a permanent sleep interval to each system cycle in Normal

Mode

0 = Normal operation

1 = Permanent sleep time added (according to the selected *SleepTime*)

Bit 1: LOW\_POWER: Low Power charging selection

0 = Normal Power charging / full-speed

1 = Low Power charging (according to the selected *LPTime*)

Bit 0: SNAP EN: Enable calculating Snap status

0 = Snap calculating is disabled

1 = Snap calculating is enabled

#### 5.6.11 Threshold Settings (0x11)

All the thresholds are setup through this address-command. This includes both the XY touch panel channels, as well as the

ProxMode channels. The meaning of these bytes are covered in Section 3.11, Section 3.12 and Section 3.13. The relative bytes are shown in the tables below:





#### Table 5.9 Threshold Settings Bytes

		1				
Byte	Data	Applicable channels	Description	Default Value		
1	ProxThreshold	Thresholds applied to trackpad	Delta value used for proximity threshold	10		
2	TouchThresholdMult	channels (falling within the selected TrackpadTx and	Touch Threshold Multiplier	5		
3	TouchThresholdShift	TrackpadRx block)	Touch Threshold Shifter	7		
4	PM ProxThreshold	ProxMode channel	Delta value used for proximity threshold of ProxMode channels	10		
5	Snap Threshold – High Byte	All channels (not	Delta value BELOW LTA where a SNAP is	100		
6	Snap Threshold – Low Byte	including ProxMode)	decided			
7	ProxThreshold2	Thresholds applied to non-trackpad	Delta value used for proximity threshold	10		
8	TouchThresholdMult2	channels (falling outside the selected	Touch Threshold Multiplier	5		
9	TouchThresholdShift2	TrackpadTx and TrackpadRx block and within the TotalRx and TotalTx block)	Touch Threshold Shifter	7		

#### 5.6.12 ATI Settings (0x12)

To configure the ATI parameters relating to the XY trackpad channels, and a second set for non-trackpad channels, this addresscommand can be used. Here the ATI Target can be set (which is then used together with

the Auto-ATI routine) to configure the ATI Compensation on each channel. An ATI C value can also be configured here for the entire touch panel. The bytes to be written are shown below.





#### Table 5.10 ATI Settings Bytes

Byte	Data	Applicable channels	Description	Default
1	ATI Target – High Byte	ATI settings applied	Target value for Count values, obtained by changing ATI	600
2	ATI Target – Low Byte	to trackpad channels (falling within the selected	compensation parameter (sensitivity)	
3	ATIC	TrackpadTx and TrackpadRx block)	ATI C value (0 to 31 decimal)	0 (values range from 0 to 31 decimal)
4	ATI Target – High Byte	ATI settings applied to non-trackpad channels (falling	Target value for Count values, obtained by changing ATI	600
5	ATI Target – Low Byte	outside the selected  TrackpadTx and TrackpadRx block	compensation parameter (sensitivity)	
6	ATIC	and within the  TotalRx and  TotalTx block)	ATI C value (0 to 31 decimal)	0 (values range from 0 to 31 decimal)

#### 5.6.13 Filter Settings (0x13)

The various settings relating to the on-chip filters can be adjusted / configured here. All these filters can be disabled if not required. Also, the damping factors (amount of filtering) can be adjusted for these independently. This is because the hover requires more filtering, whereby the touch points have either a static or dynamic filter implementation. The

damping parameter for the touch and hover co-ordinate, and ProxMode count filters are used as this value/256. The normal mode count filter damping value is 1/2<sup>value</sup>. The smaller these fractions, the MORE filtering will occur. More filtering provides stable data at the cost of responsiveness.

The bytes and definitions are provided.





#### **Table 5.11 Filter Settings Bytes**

Byte	Data	Description	Default Value
1	FilterSettings0	Numerous filter settings	0x00
2	Touch Filter Damping Value	Adjust the amount of filtering applied to the XY touch points	128 (128/256)
3	Hover Filter Damping Value	Adjust the amount of filtering applied to the XY hover points	38 (128/256)
4	PM Count Filter Damping Value	Adjusts the amount of filtering applied to the ProxMode count value	16 (128/256)
5	LP PM Count Filter Damping Value	Adjusts the amount of filtering applied to the ProxMode count value – In Low-Power charging	128 (128/256)
6	NM Count Filter Damping Value	Adjusts the amount of filtering applied to the normal mode count values	3 (1/2³)

	FilterSettings0									
Bit	7 6 5 4			4	3	2	1	0		
Name				DIS_NM_ FILTER	DIS_PM_ FILTER	SELECT_TOUCH_ FILTER	DIS_HOVER_ FILTER	DIS_TOUCH_ FILTER		
Default	-	-	-	-	0	0	0	0		

Bit 7-5: Unused

Bit 4: DIS\_NM\_FILTER: Disable Normal Mode count value filter

0 = Filter enabled

1 = Filter disabled

Bit 3: DIS\_PM\_FILTER: Disable ProxMode count value filter

0 = Filter enabled

1 = Filter disabled

Bit 2: SELECT\_TOUCH\_FILTER: Select the type of filtering for the touch co-ordinates

0 = Dynamic filter (variable damping factor relative to XY motion)

1 = Static filter (fixed but configurable damping factor)

Bit 1: DIS\_HOVER\_FILTER: Disable filtering on hover co-ordinates

0 = Hover filtering enabled

1 = Hover filtering disabled





DIS TOUCH FILTER: Disable filtering on touch co-ordinates Bit 0:

0 = Touch filtering enabled

1 = Touch filtering disabled

#### 5.6.14 Timing Settings (0x14)

On-chip timings can be adjusted here, and are explained below.

#### **Reseed Time**

The reseed time is the time that a channel allows a prox/touch to continually be set, before assuming it is a fault condition. Once this time has elapsed, a reseed is forced to correct the condition. In Normal Mode, all the including the ProxMode channels (not channel) are reseeded. For the default value, if a prox/touch is seen anywhere on the touch panel for 40s, a global reseed will be executed to remove this assumed stuck condition. It is recommended to make this timeout quite long, since it is unpleasant to have the device reseed while drawing on the panel. master can however still send the Reseed command anytime when fault conditions relative to the application are assumed.

When this occurs in ProxMode, only the ProxMode channel is reseeded.

The configurable value works in multiples of 500ms, thus the default value of 80, will select a ReseedTime of 40s.

#### **Comms Timeout**

This is a timer that monitors the activity on the I<sup>2</sup>C bus. If there is no successful read/write this within this time a timeout will occur, and communication window will close, resuming sensing.

assists overcoming in any master cannot always service communication, and does not mind missing a

cycle's data, then the RDY will only be high for the Comms Timeout time, and then it will go low, and the cycle will continue.

#### **Mode Time**

This timer is responsible for the timings used when implementing the Automatic Mode control, and also the low-power timings. The value used here is also a multiple of 500ms. For more details see Section 3.4 and Section 3.5

#### **Low-Power Time**

Here the low-power timing can be adjusted. Predetermined intervals can be selected here to configure how long the device will sleep between data acquisitions in the Low-Power charging mode. Clearly the longer the sleep time, the lower the power consumption will be, the expense of response Typically the ProxMode channel will be setup to provide a good distance proximity. This will give the device enough time to be up and running before the user interacts with the trackpad channels, even if the LPTime is selected to be reasonably long.

#### Sleep Time

This is a sleep interval that is permanently added to each cycle in Normal Mode, unlike the Low-Power which is only added under certain conditions (for example if prox/touch is sensed). This can be used in current sensitive designs, where a rapid response rate is not stuck For example, if a normal cycle took ~10ms in conditions caused by errors on the I<sup>2</sup>C bus. It total, then adding a 10ms SleepTime could can also be used as a RDY timeout, thus if the almost halve the total current consumption. the This would add to any Low-Power timing.





**Table 5.12 Timing Settings Bytes** 

Byte	Data	Description	Default Value
1	ReseedTime	Stuck prox/touch condition timer	80 (thus 40s)
2	CommsTimeout	Timer monitoring the I <sup>2</sup> C activity	100 (ms)
3	ModeTime	Mode timer (Mode switching, and Low-Power switching)	8 (thus 4s)
4	LPTime	Low-Power timing	8 <sup>Note1</sup> (~160ms)
5	SleepTime	Permanent sleep timing	3 <sup>Note1</sup> (~5ms)

Note1: Both of these values are translated to the time interval, as depicted in Table 5.13.

Table 5.13 LPTime and SleepTime Selections

Value	Time	Value	Time	
1	1 ms	7	80 ms	
2	2 ms	8	160 ms	
3	5 ms	9	320 ms	
4	10 ms	10	640 ms	
5	20 ms	11	1.2 s	
6	40 ms	12	2.4 s	
		13	5 s	

Note: These are only approximate values, and must not be used for time critical applications.

#### **5.6.15 Channel Setup (0x15)**

The amount of Rx's and Tx's used for trackpad (XY information) purposes, as well as the total Rx and Tx's used can be selected. Note that many of the data packets are variable in length relative to how many Rx's

and Tx's are used. Thus once these are changed, remember that the amount of data available under certain address-commands could also change.





#### Table 5.14 Channel Setup Bytes

Byte	Data	Description Default			Value		
			IQS550	IQS525	IQS512		
1	TotalRxs	Total amount of Rx channels used	10	5	4		
2	TotalTxs	Total amount of Tx channels used	15	5	3		
3	TrackpadRxs	Total amount of Rx channels used for XY trackpad purposes	10	5	4		
4	TrackpadTxs	Total amount of Tx channels used for XY trackpad purposes	15	5	3		
5	PMSetup0	ProxMode settings, and Rx configuration	40 (hex)	0	0		
6	TxHigh	DrayMada Ty configuration	7F (hex)	0	0		
7	TxLow	ProxMode Tx configuration	FF (hex)	1F (hex)	07 (hex)		

	PMSetup0									
Bit	7 6			4	3	2	1	0		
Name	CHARGE_TYPE	RX_GROUP (only on IQS550)	~	SUM_OF_ TP (only on IQS512/525)	RX_SELECT					
Default	0	1	0	0	0	0	0	0		

Bit 7: CHARGE\_TYPE: Select ProxMode method of charging

0 = Projected/Mutual Capacitive Charging

1 = Self Capacitive Charging

Bit 6: RX\_GROUP: Select RxA or RxB for ProxMode channel electrode

0 = ProxMode channel is from RxA group

1 = ProxMode channel is from RxB group

Bit 5: Unused

Bit 4: SUM\_OF\_TP: Configure ProxMode channel as sum of trackpad channels

0 = ProxMode channel defined by RX SELECT and TxHigh:TxLow

1 = ProxMode is sum of Rx's falling within selected *TrackpadRxs*.

CHARGE\_TYPE and TxHigh:TxLow still select as usual.

Bit -0-3: RX SELECT: Select individual Rx electrode for ProxMode

0 - 9 = Decimal value translates to Rx0 to Rx9





		TxHigh <sup>(1)</sup>										
Bit	7	6	5	4	3	2	1	0				
Name	~	PM_TX14	PM_TX13	PM_TX12	PM_TX11	PM_TX10	PM_TX9	PM_TX8				
Default	0	1	1	1	1	1	1	1				

Bit 7: Unused

Bit 6-0: PM\_TX: Select active Tx's for ProxMode

0 = Disabled1 = Enabled

	TxLow <sup>(1)</sup>										
Bit	7	6	5	4	3	2	1	0			
Name	PM_TX7	PM_TX6	PM_TX5	PM_TX4	PM_TX3	PM_TX2	PM_TX1	PM_TX0			
Default	1	1	1	1	1	1	1	1			

Bit 7-0: PM\_TX: Select active Tx's for ProxMode

0 = Disabled

1 = Enabled

Note1: If self capacitive charging is selected, the Rx channel is used and the Tx selection is redundant.

#### 5.6.16 Hardware Config Settings (0x16)

Settings specific to the ProxSense<sup>®</sup> Module charge transfer characteristics can be changed. It is however unlikely that the designer will change these values from the default settings, and thus they are just briefly mentioned.

A few settings that might be required: ProxSettings0<NOISE\_EN>. This is the onchip noise detection. It can be enabled and disabled here (0=disabled, 1=enabled).

The ProxSense® charge transfer frequency can be setup here by changing the UPLEN and PASSLEN parameters. The charge transfer frequency (f<sub>cc</sub>) can be calculated as:

$$f_{cc} = \frac{16.10^6}{(2^{(7-CK\_FREQ)} \times (2 + UP + PASS + INC\_PHASE)}$$

where

$$UP = 2^{(UPLEN-2)}$$
 (if UPLEN > 4)  
 $UP = UPLEN$  (if UPLEN  $\leq$  4)  
 $PASS = 2^{(PASSLEN-2)}$  (if PASSLEN > 4)  
 $PASS = PASSLEN$  (if PASSLEN  $\leq$  4)

Note: CK\_FREQ, UPLEN and PASSLEN are the numerical value of the settings

For example the default frequency is:

$$f_{cc} = \frac{16.10^6}{(2^{(7-7)} \times (2+4+3+0)} = 1.77MHz$$





## **Table 5.15 Prox Hardware Config**

Byte	Data	Description	Default Value
1	ProxSettings0	Prox Module Settings Byte 0	0x24
2	ProxSettings1	Prox Module Settings Byte 1	0x72
3	ProxSettings2	Prox Module Settings Byte 2	0x15
4	ProxSettings3	Prox Module Settings Byte 3	0x43

		ProxSettings0							
Bit	7	6	5	4	3	2	1	0	
Name	~	~	NOISE_EN	~	~	CXVSS	SYNC_EN	SYNC_EDGE	
Default	0	0	1	0	0	1	0	0	

				Pr	oxSetti	ings1		
Bit	7	6	5	4	3	2	1	0
Name	MODULE_LP	CK_	CK_FREQ<2:0>		~	~	ANA_DEAD	INC_PHASE
Default	0	1	1	1	0	0	1	0

	ProxSettings2							
Bit	7	6	5	4	3	2	1	0
Name	STBL_1	STBL_0	OP_BIAS1	OP_BIAS0	TRIP3	TRIP2	TRIP1	TRIP0
Default	0	0	0	1	0	1	0	1

	ProxSettings3							
Bit	7	6	5	4	3	2	1	0
Name	~	UPLEN2	UPLEN1	UPLEN0	~	PASSLEN2	PASSLEN1	PASSLEN0
Default	0	1	0	0	0	0	1	1





### 5.6.17 Active Channels (0x17)

0 = Disabled 1 = Enabled

Here individual channels can be disabled. By clearing the relative bit in the Active Channels byte a channel disable is achieved.

**Table 5.16 Active Channel Bytes** 

Byte	Data	Description		Default	
			IQS550	IQS525	IQS512
1	ActiveChannels[Tx0] – High Byte	Active Channels	0x3FF	0x01F	0x00F
2	ActiveChannels [Tx0] - Low Byte	bits (Rx0 = bit0, Rx1 = bit1)			
3	ActiveChannels [Tx1] - High Byte	Active Channels bits (Rx0 = bit0, Rx1	0x3FF	0x01F	0x00F
4	ActiveChannels [Tx1] - Low Byte	= bit1)			
:	:	:	0x3FF	0x01F	0x00F
(2 x TotalTxs) - 1	ActiveChannels [Last Tx] – High Byte	Active Channels bits (Rx0 = bit0, Rx1 = bit1)	0x3FF	0x01F	0x00F
(2 x TotalTxs)	ActiveChannels [Last Tx] - Low Byte				

## 5.6.18 Debounce Settings (0x18)

The debounce parameters of the channel outputs can be configured here.

**Table 5.17 Debounce Value Bytes** 

Byte	Data	Description	Default
1	ProxDb	Proximity debounce values	0x44 (Db = 4)
2	TouchSnapDb	Touch and snap debounce values	0x44 (Touch db = 0 Snap db = 1)





				Pro	xDb			
Bit	7	6	5	4	3	2	1	0
Name	Prox Set Debounce					Prox Clear	Debounce	
Default	0	1	0	0	0	1	0	0

		TouchSnapDb						
Bit	7	6	5	4	3	2	1	0
Name		Set ounce	Touch Set Debounce		Click Debo	Clear	Touch Debo	
Default	0	1	0	0	0	1	0	0

### **5.6.19 ProxMode Proximity Status (0x20)**

The proximity of the ProxMode channel can be obtained here.

**Table 5.18 PM Proximity Status Bytes** 

Byte	Data	Description
1	PM Prox Byte	0 = No Prox / 1 = ProxMode Channel Prox

### 5.6.20 ProxMode Count Data Read (0x21)

When this address-command is setup, the count value for the ProxMode channel can be read.

The count value consists of 2 bytes. Normally this is only read while setting up the ATI parameters

Table 5.19 ProxMode Count Values

Byte	Data	Description
1	PM Count Value – High Byte	ProxMode count value for self capacitive prox channel
2	PM Count Value – Low Byte	sell capacitive prox channel

# **5.6.21 ProxMode** Long-Term Average be read. The Long-Term Average again consists of 2 bytes.

When this address-command is the Long-Term-Average for the ProxMode channel can





### Table 5.20 PM Long-Term Average Bytes

Byte	Data	Description
1	PM Long-Term Average – High Byte	ProxMode Long-Term Average for self capacitive
2	PM Long-Term Average – Low Byte	prox channel

#### 5.6.22 ProxMode ATI Compensation 5.6.23 ProxMode ATI Settings (0x24) Read/Write (0x23)

address-command. process and the recommended. single byte.

To configure the ProxMode ATI parameters, To read or write the ProxMode channels' ATI this address-command can be used. Here the Compensation value, a read/write must be ATI target can be configured (which is then performed after setting up the corresponding used together with the Auto-ATI routine) to Configuring the ATI configure the ATI Compensation on the Compensation manually is however a lengthy ProxMode channel. An ATI C value can also Auto-ATI routine is be configured for the ProxMode channel. The The ATI Compensation is a bytes to be written are shown below.

Table 5.21 ATI Settings Bytes

Byte	Data	Description	Default Value
1	PM ATI Target – High Byte	Target value for Count	500
2	PM ATI Target – Low Byte	values, obtained by changing ATI compensation parameter (sensitivity)	
3	PM ATIC	ATI C value	0 (values from 0 to 31)





## 6 Circuit Diagram

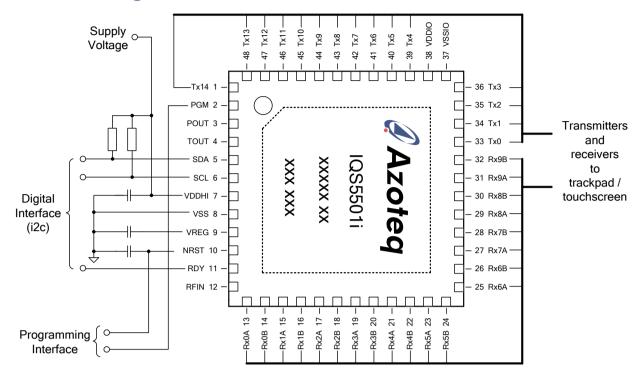


Figure 6.1 IQS550 Overview Diagram

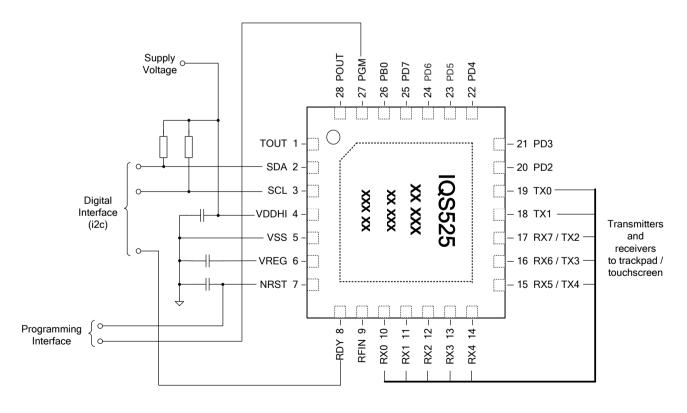


Figure 6.2 IQS525 Overview Diagram





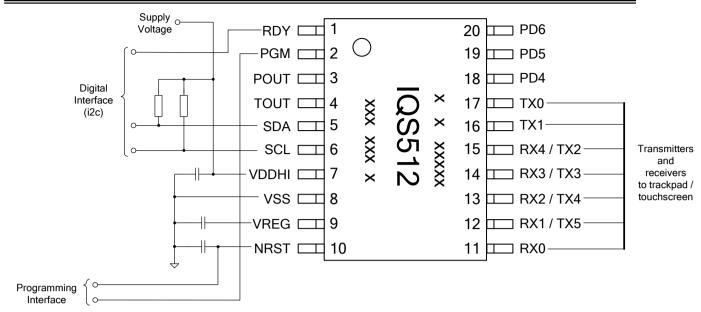


Figure 6.3 IQS512 Overview Diagram





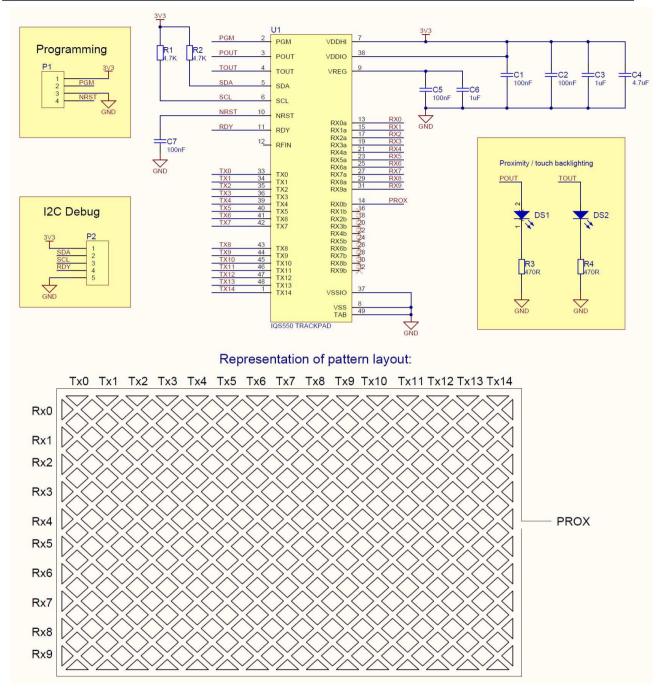


Figure 6.4 IQS550 Application Circuit





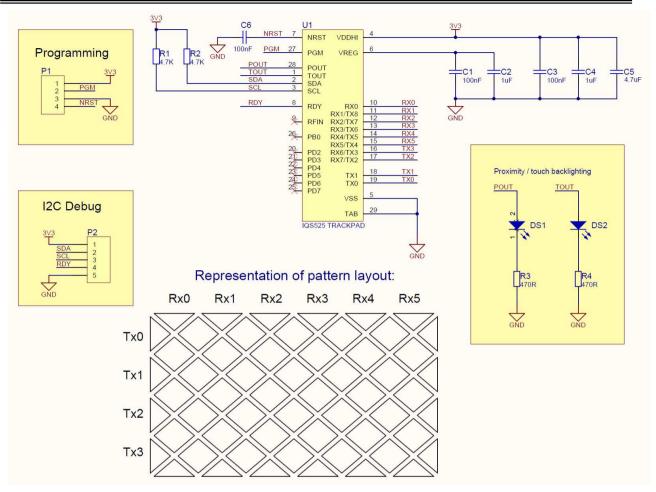


Figure 6.5 IQS525 Application Circuit





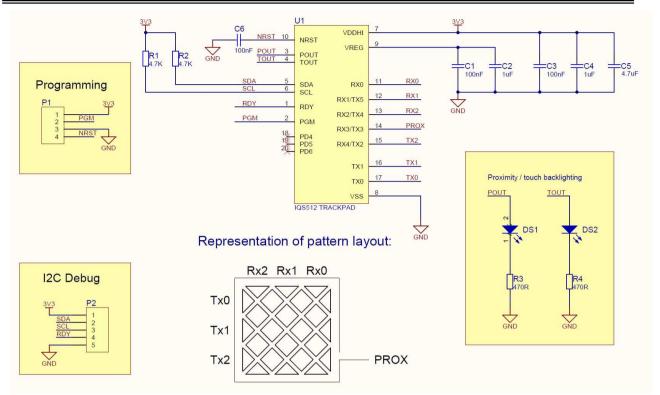


Figure 6.6 IQS512 Application Circuit



### 7 Electrical Characteristics

### 7.1 Absolute Maximum Ratings

Exceeding these maximum ratings may cause permanent damage to the device.

**Table 7.1 Voltage Characteristics** 

Symbol	Rating	Min	Max	Unit	
V <sub>DDHI</sub> - V <sub>SS</sub>	External supply voltage	-0.3	4.0		
	Receiver channel pins (Rx0ARx9B) V <sub>SS</sub> -0.3		V <sub>REG</sub> (-1.55)		
	Input voltage on transmit pins (Tx0Tx14))	PXS off	V <sub>SS</sub> -0.3	4.0	V
V <sub>IN</sub>	PXS on <sup>(1)</sup>		V <sub>SS</sub> -0.3	V <sub>REG</sub> (-1.55)	
	Input voltage on any pin <sup>(2)</sup>		V <sub>SS</sub> -0.3	4.0	

- 1. If the ProxSense® peripheral is on, no injection must be performed on any pin having the transmit function (Tx) as an alternate function, even if this alternate function is not specified
- I<sub>INJ(PIN)</sub> must never be exceeded. This is implicitly insured if V<sub>IN</sub> maximum is respected. If V<sub>IN</sub> maximum cannot be respected, the injection current must be limited externally to the I<sub>INJ(PIN)</sub> value. A positive injection is induced by V<sub>IN</sub>>V<sub>DDHI</sub> while a negative is induced by V<sub>IN</sub><V<sub>SS</sub>.

**Table 7.2** Current Characteristics

Symbol	Rating	Max.	Unit
I <sub>VDDHI</sub>	Total current into V <sub>DDHI</sub> power line (source)	80	
I <sub>vss</sub>	Total current out of V <sub>SS</sub> ground line (sink)	80	
	Output current sunk by any other I/O and control pin	25	A
I <sub>IO</sub>	Output current source by any I/Os and control pin	-25	mA
I <sub>INJ(PIN)</sub> <sup>(1)</sup>	Injected current on any pin <sup>(2)</sup>	±5	
∑ I <sub>INJ(PIN)</sub> <sup>(1)</sup>	Total injected current (sum of all I/O and control pins) <sup>(2)</sup>	±25	

- 1.  $I_{\text{INJ(PIN)}}$  must never be exceeded. This is implicitly insured if  $V_{\text{IN}}$  maximum is respected. If  $V_{\text{IN}}$  maximum cannot be respected, the injection current must be limited externally to the  $_{\text{IINJ(PIN)}}$  value. A positive injection is induced by  $V_{\text{IN}} > V_{\text{DDHI}}$  while a negative injection is induced by  $V_{\text{IN}} < V_{\text{SS}}$ . For true open-drain pads, there is no positive injection current, and the corresponding  $V_{\text{IN}}$  maximum must always be respected.
- 2. When several inputs are submitted to a current injection, the maximum  $\Sigma I_{INJ(PIN)}$  is the absolute sum of the positive and negative injected currents (instantaneous values). These results are based on characterization with  $\Sigma I_{INJ(PIN)}$  maximum current injection on four I/O port pins of the device.





**Table 7.3** Thermal Characteristics

Symbol	Rating	Max.	Unit
TSTG	Storage temperature range	-65 to +150	°C
TJ	Maximum junction temperature	150	C

### 7.2 Operating Conditions

### 7.2.1 General Operating Conditions

**Table 7.4** General Operating Conditions

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
f <sub>MASTER</sub> <sup>(1)</sup>	Master clock frequency	ency 1.65V ≤ V <sub>DDHI</sub> - 16 ≤3.6V		-	MHz	
$V_{\text{DDHI}}$	Standard operating voltage	-	1.65	1.65		V
P <sub>D</sub> <sup>(2)</sup>	Power dissipation at T <sub>A</sub> = 85°C	-	-	-	625	mW
T <sub>A</sub>	Temperature range	1.65V ≤ V <sub>DDHI</sub> ≤3.6V	-40	-	85	°C
T <sub>J</sub>	Junction temperature range	-40°C ≤ V <sub>DDHI</sub> ≤ 85°C	-40	-	105	°C

<sup>1.</sup>  $f_{MASTER} = f_{CPU}$ 

### 7.2.2 Power-up / Power-down Operating Conditions

Table 7.5 Operating conditions at power up / down

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
t <sub>VDDHI</sub>	V <sub>DDHI</sub> rise time rate		20	-	1300	μs/V
t <sub>TEMP</sub>	Reset release decay	V <sub>DDHI</sub> rising	-	1	-	Ms
V <sub>POR</sub>	Power on reset threshold		1.44	-	1.65 <sup>(1)</sup>	V
V <sub>PDR</sub>	Power down reset threshold		1.30	-	1.60 <sup>(2)</sup>	V

<sup>1.</sup> Tested in production

<sup>2.</sup> To calculate  $P_{Dmax}(T_A)$  use the formula given in thermal characteristics  $P_{Dmax}=(T_{Jmax}-T_A)/\theta_{JA}$  with  $T_{Jmax}$  in this table and  $\theta_{JA}$  in table "Thermal characteristics".

<sup>2.</sup> Data based on characterisation results, not tested in production.





### 7.2.3 Supply current characteristic

Table 7.6 Total Current Consumption<sup>(1)</sup>

Cymah al	Charging	Low-Power	С	urrent (Typ	p)	Unit
Symbol	Mode	setting	IQS550	IQS525	IQS512	Unit
	Normal Mode	LP disabled	4.20 <sup>(3)</sup>	3.60	3.70	mA
	Normal Mode <sup>(2)</sup>	10ms	3.30	1.85	1.30	mA
	Normal Mode <sup>(2)</sup>	20ms	2.60	1.30	0.86	mA
	Normal Mode <sup>(2)</sup>	40ms	2.00	0.85	0.60	mA
	Normal Mode <sup>(2)</sup>	80ms	1.40	0.44	0.42	mA
	Normal Mode <sup>(2)</sup>	320ms	330	320	300	uA
I <sub>DD</sub>	ProxMode	LP disabled	1.84		mA	
	ProxMode <sup>(3)</sup>	10ms		372		uA
	ProxMode <sup>(3)</sup>	40ms		110		uA
	ProxMode <sup>(3)</sup>	80ms		57		uA
	ProxMode <sup>(3)</sup>	320ms	15			uA
	ProxMode <sup>(3)</sup>	640ms		8		uA
	ProxMode <sup>(4)</sup>	640ms		10		uA

- 1. Based on bench measurements, not characterised
- 2. Tested with maximum number of sensors active (IQS550 15x10 / IQS525 5x5 / IQS512 4x3); ATI Target of 600 counts; Event-Mode enabled (thus no communication)
- 3. Tested with a ProxMode channel configured in self capacitive mode; ATI Target of 1000; and Event-Mode enabled
- 4. Tested with a ProxMode channel configured in projected capacitive mode (all Tx's active); ATI Target of 1000; and Event-Mode enabled



# IQ Switch<sup>®</sup> ProxSense<sup>®</sup> Series



### 7.2.4 Report Rate

Table 7.7 Example Report Rates<sup>1</sup>

Device Total Rxs Total		Total Tva		Report Rate (Hz)		
Device	TOTAL RXS	Total Txs	1 touch <sup>2</sup>	2 touches <sup>3</sup>	5 touches <sup>4</sup>	
IQS550	10	15	129	112	88	
IQS550	9	12	161	137	102	
IQS550	9	9	192	165	113	
IQS525	5	5	333	255	-	
IQS525	6	4	357	231	-	
IQS525	3	3	416	300	-	
IQS512	4	3	588	406	-	
IQS512	3	3	625	427	-	

- Based on bench measurements, not characterised.
   Tested with ATI Target of 600 counts. Only relevant amount of XY data streamed via I<sup>2</sup>C.
- 2. Stream 1 co-ordinates data (total of 8 bytes)
- 3. Stream 2 co-ordinates data (total of 15 bytes)
- 4. Stream 5 co-ordinates data (total of 36 bytes)

### 7.2.5 I/O port pin characteristics

#### **General characteristics**

Subject to general operating conditions for  $V_{\text{DDHI}}$  and  $T_{\text{A}}$  unless otherwise specified. All unused pins must be kept at a fixed voltage: using the output mode of the I/O for example or an external pull-up or pull-down resistor.





### Table 7.8 Standard I/O Static characteristic (1) (2)

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Unit
V <sub>IL</sub>	Input low level voltage <sup>(3)</sup>	Standard I/Os	V <sub>SS</sub> -0.3	-	0.3V <sub>DDHI</sub>	V
V <sub>IH</sub>	Input high level voltage <sup>(3)</sup>	Standard I/Os	0.7 V <sub>DDHI</sub>	-	V <sub>DDHI</sub> +0.3	
$V_{hys}$	Schmitt trigger voltage hysteresis <sup>(4)</sup>	Standard I/Os	-	200	-	mV
I <sub>lkg</sub>	Input leakage current <sup>(5)</sup>	V <sub>SS</sub> ≤ V <sub>IN</sub> ≤ V <sub>DDHI</sub> Standard I/Os	-1	-	1	uA
		$V_{SS} \le V_{in} \le V_{REG}^{(6)}$ Rx, Tx I/Os	-1	-	1	
R <sub>PU</sub>	Weak pull-up equivalent resistor <sup>(7)</sup>	$V_{IN} = V_{SS}$	30	45	60	kΩ
C <sub>IO</sub> (8)	I/O pin capacitance		-	5	-	pF

- 1.  $V_{DDHI} = 3.0 \text{ V}$ ,  $T_A = -40 \text{ to } 85^{\circ}\text{C}$  unless otherwise specified.
- 2. Not applicable to Rx and Tx pins.
- 3. Data based on characterisation results, not tested in production.
- 4. Hysteresis voltage between Schmitt trigger switching levels. Based on characterization results, not tested.
- 5. The maximum value may be exceeded if negative current is injected on adjacent pins.
- 6.  $V_{IN}$  must not exceed  $V_{REG}$  value if ProxSense<sup>®</sup> is enabled, even on port B and D (Tx),  $V_{REG}$  = 1.55V.
- 7.  $R_{PU}$  pull-up equivalent resistor based on a resistive transistor (corresponding  $I_{PU}$  current characteristics)
- 8. Data guaranteed by design, not tested in production





### **Output driving current**

Subject to general operating conditions for V<sub>DDHI</sub> and T<sub>A</sub> unless otherwise specified.

**Table 7.9** Output driving current (high sink ports)

I/O type	Symbol	Parameter Conditions	Conditions	Min.	Max.	Unit
			$I_{IO}$ = +2mA, $V_{DDHI}$ = 1.8V	-	0.45	
	V <sub>OL</sub> <sup>(1)</sup>	Output low level voltage for an I/O pin	$I_{IO}$ = +2mA, $V_{DDHI}$ = 3.0V	-	0.45	
Standard			$I_{IO}$ = +10mA, $V_{DDHI}$ = 3.0V	-	0.7	
Standard		V <sub>OH</sub> <sup>(2)</sup> Output high level voltage for an I/O pin	$I_{IO}$ = -1mA, $V_{DDHI}$ = 1.8V	V <sub>DDHI</sub> -0.45	ı	
	$V_{\text{OH}}^{(2)}$		$I_{IO} = -1 \text{mA},$ $V_{DDHI} = 3.0 \text{V}$	V <sub>DDHI</sub> -0.45	-	V
			$I_{IO}$ = -10mA, $V_{DDHI}$ = 3.0V	V <sub>DDHI</sub> -0.7	-	
ProxSense I/O	V <sub>OL</sub>	Output low level voltage for Tx and Rx ProxSense I/Os	I <sub>RX</sub> = TBD	ı	TBD	
	V <sub>OH</sub>	Output high level voltage for Tx ProxSense I/O	I <sub>TX</sub> = 1mA	1.45	-	
	V <sub>OH</sub>	Output high level voltage for Rx ProxSense I/O	I <sub>PXS_RX</sub> = 0.5mA	1.35	-	

<sup>1.</sup> The  $I_{IO}$  current sunk must always respect the absolute maximum rating and the sum of  $I_{IO}$  (I/O ports and control pins) must not exceed  $I_{VSS}$ .

### **NRST** pin

The NRST pin input driver is CMOS. A permanent pull-up is present, thus an external component is not needed if NRST is unconnected in the design.

Subject to general operating conditions for V<sub>DDHI</sub> and T<sub>A</sub> unless otherwise specified.

<sup>2.</sup> The  $I_{IO}$  current sourced must always respect the absolute maximum rating and the sum of  $I_{IO}$  (I/O ports and control pins) must not exceed  $I_{VDDHI}$ .





### Table 7.10 NRST pin characteristics

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Unit
$V_{\text{IL}(\text{NRST})}$	NRST Input low level voltage <sup>(1)</sup>		$V_{SS}$	-	0.8	
V <sub>IH(NRST)</sub>	NRST Input high level voltage <sup>(1)</sup>		1.4	-	$V_{\text{DDHI}}$	V
V <sub>OL(NRST)</sub>	NRST Output low level voltage	I <sub>OL</sub> = 2mA	-	-	V <sub>DDHI</sub> - 0.8	
R <sub>PU(NRST)</sub>	NRST pull-up equivalent resistor <sup>(2)</sup>		30	45	60	kΩ
V <sub>F(NRST)</sub>	NRST input filtered pulse <sup>(3)</sup>		-	-	50	
t <sub>OP(NRST)</sub>	NRST output pulse width		20	-	-	ns
V <sub>NF(NRST)</sub>	NRST input not filtered pulse <sup>(3)</sup>		300	-	-	

- 1. Data based on characterization results, not tested in production.
- 2. The RPU pull-up equivalent resistor is based on a resistive transistor.
- 3. Data guaranteed by design, not tested in production.

The reset network shown in Figure 7.1 protects the device against parasitic resets. The user must ensure that the level on the NRST pin can go below the  $V_{IL}$  max. level specified in *Table 7.10*. Otherwise the reset is not taken into account internally.

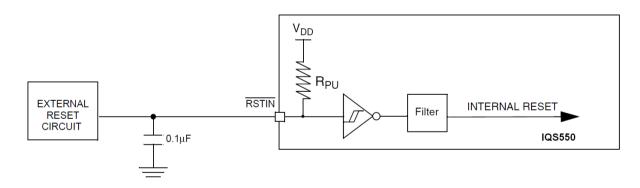


Figure 7.1 Recommended NRST pin configuration

#### 7.2.6 Electrostatic discharge (ESD)

Electrostatic discharges (a positive then a negative pulse separated by 1 second) are applied to the pins of each sample according to each pin combination. The sample size depends on the number of supply pins in the device (3 parts\*(n+1) supply pin). Two models can be simulated: human body model and charge device model. This test conforms to the JESD22-A114A/A115A standard.





Table 7.11 ESD Absolute Maximum Ratings

Symbol	Ratings	Conditions	Max Value	Unit
V <sub>ESD</sub> (HBM)	Electrostatic discharge voltage (human body model)		2000 <sup>(2)</sup>	
V <sub>ESD</sub> (CDM)	Electrostatic discharge voltage (charge device model)	T <sub>A</sub> = +25 °C	1000	V

- 1. Data based on characterisation results, not tested in production.
- 2. Device sustained up to 3000 V during ESD trials.

#### 7.2.7 Thermal characteristics

The maximum chip junction temperature  $(T_{Jmax})$  must never exceed the values given in Table 7.4.

The maximum chip-junction temperature,  $T_{Jmax}$ , in degrees Celsius, may be calculated using the following equation:

$$T_{Jmax} = T_{Amax} + (P_{Dmax} \times \theta_{JA})$$

#### Where:

- T<sub>Amax</sub> is the maximum ambient temperature in °C
- ullet  $\theta_{JA}$  is the package junction-to-ambient thermal resistance in °C/W
- $P_{Dmax}$  is the sum of  $P_{INTmax}$  and  $P_{I/Omax}$  ( $P_{Dmax} = P_{INTmax} + P_{I/Omax}$ )
- $\bullet$  P<sub>INTmax</sub> is the product of I<sub>DD</sub> and V<sub>DDHI</sub>, expressed in watts. This is the maximum chip internal power.
- $P_{I/Omax}$  represents the maximum power dissipation on output pins where:  $P_{I/Omax} = \sum (V_{OL}*I_{OL}) + \sum ((V_{DDHI} V_{OH})*I_{OH})$ , taking into account the actual  $V_{OL}/I_{OL}$  and  $V_{OH}/I_{OH}$  of the I/Os at low and high level in the application.

Table 7.12 Thermal characteristics<sup>(1)</sup>

Symbol	Parameter	Value	Unit
$\Theta_{JA}$	Thermal resistance junction ambient	32	°C/W

1. Thermal resistances are based on JEDEC JESD51-2 with 4-layer PCB in a natural convection environment.



## 8 Mechanical Dimensions

## 8.1 IQS550 QFN(7x7)-48 Mechanical Dimensions

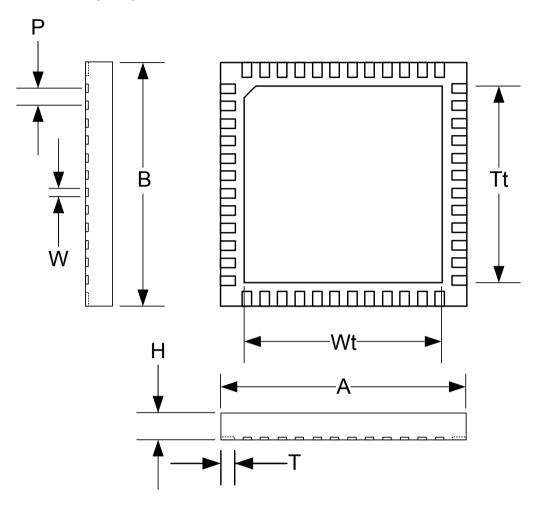


Figure 8.1 QFN(7x7)-48 Package

**Table 8.1** Dimensions from Figure 8.1

Label	Dimension (mm)	Label	Dimension (mm)	
Р	0.50	Wt (max)	5.65	
T (min)	0.30	Ft (max)	0.35	
T (max)	0.50	A (min) / B (min)	6.90	
W (min)	0.20	A (max) / B (max)	7.10	
W (max)	0.30	H (max)	0.6	
Tt (max)	5.65			



## 8.2 IQS550 Landing Pad Layout

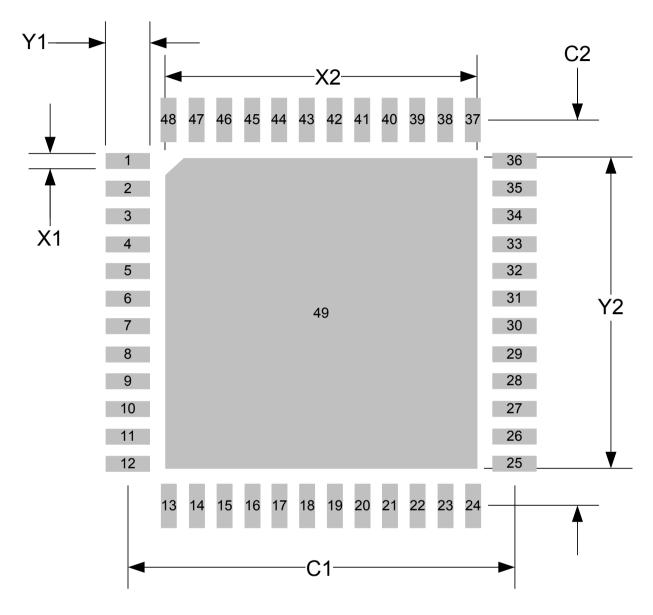


Figure 8.2 QFN(7x7)-48 Footprint

**Table 8.2** Dimensions from Figure 8.2

Label	Dimension (mm)	Label	Dimension (mm)
C1	7.00	C2	7.00
Y1	0.50	Y2	5.65
X1	0.30	X2	5.65

\*Note: Pin 49 = Vss



## 8.3 IQS525 QFN(4x4)-28 Mechanical Dimensions

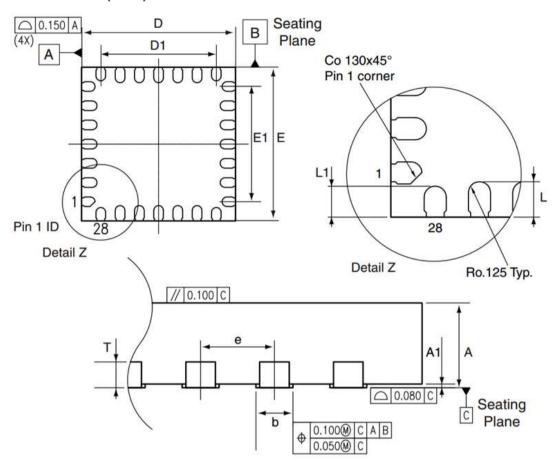


Figure 8.3 QFN(4x4)-28 Package

**Table 8.3** Dimensions from Figure 8.3

Label	Dimension (mm)			Label	Dimension (mm)		
Labei	Min	Typical	Max	Labei	Min	Typical	Max
А	0.5	0.55	0.6	L	0.3	0.4	0.5
A1	-0.05	0	0.05	L1	0.25	0.35	0.45
D	3.9	4.0	4.1	Т		0.152	
D1	2.9	3.0	3.1	b	0.2	0.25	0.3
E	3.9	4.0	4.1	е		0.5	
E1	2.9	3.0	3.1				





## 8.4 IQS525 Landing Pad Layout

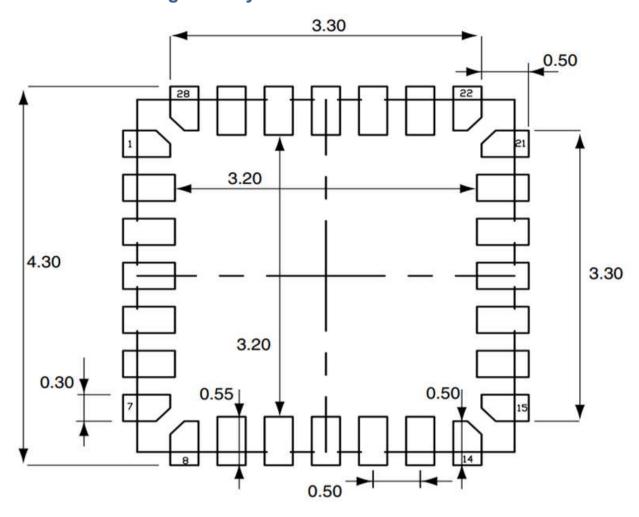


Figure 8.4 QFN(4x4)-48 Footprint (dimensions in millimeters)



### 8.5 IQS512 TSSOP20 Mechanical Dimensions

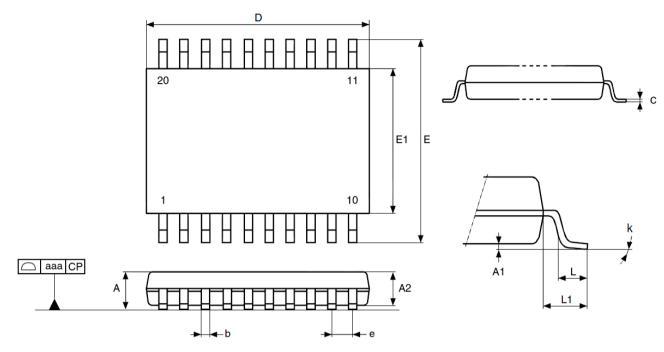


Figure 8.5 TSSOP20 Package

Table 8.4 Dimensions from Figure 8.5

Lobel	Dimension (mm)			Label	Dimension (mm)		
Label	Min	Typical	Max	Labei	Min	Typical	Max
А			1.2	E1	4.3	4.4	4.5
A1	0.05		0.15	е		0.65	
A2	0.8	1.0	1.05	L	0.45	0.60	0.75
b	0.19		0.30	L1		1.0	
С	0.09		0.20	k	0.0°		8.0°
D	6.4	6.5	6.6				
E	6.2	6.4	6.6				0.1



## 8.6 IQS512 Landing Pad Layout

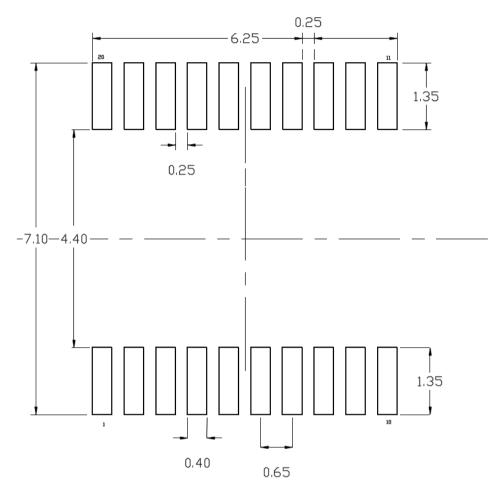


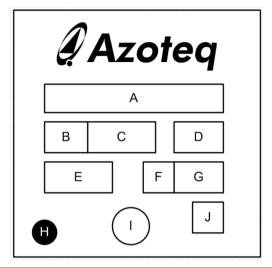
Figure 8.6 TSSOP Footprint (dimensions in millimeters)





# 9 Device Marking

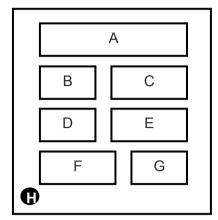
## 9.1 IQS550 Marking



IC Name Α В **Assembly Plant** Internal use Internal use D Е Country of Origin Assembly Year = Assembly Week G Dot - Pin1 reference Internal use = Design Revision

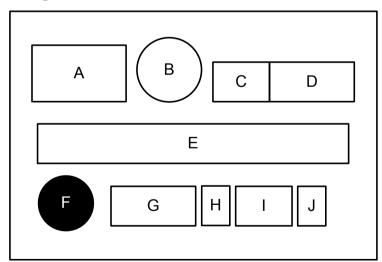


## 9.2 IQS525 Marking



A	=	IC Name
В	=	Assembly Plant
С	=	Internal use
D	=	Internal use
E	=	Country of Origin
F	=	Assembly Date
G	=	Additional Information including Design Revision Code
н	=	Dot – Pin1 reference

## 9.3 IQS512 Marking



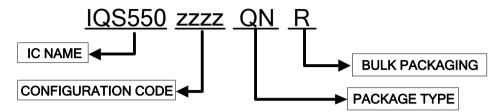
Α	=	-	F	=	Dot – Pin1 reference
В	=	Internal use	G	=	Country of Origin
С	=	Internal use	Н	=	Assembly Year
D	=	Internal use	- 1	=	Assembly Week
Е	=	IC Name	J	=	Design Revision



## **10 Ordering Information**

Order quantities will be subject to multiples of full reels. For large orders, Azoteq can provide custom configured devices.

## 10.1 IQS550 Ordering



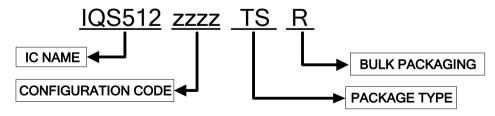
IC NAME	IQS550	=	IQS550
CONFIGURATION CODE	A000 0000	= =	Preprogrammed (trackpad specific firmware) Unprogrammed
PACKAGE TYPE	QN	=	QFN(7x7)-48
BULK PACKAGING	R	=	Reel (2500pcs/reel)

### 10.2 IQS525 Ordering



IC NAME	IQS525	=	IQS525
CONFIGURATION CODE	0000	=	Unprogrammed
PACKAGE TYPE	QN	=	QFN(4x4)-28
BULK PACKAGING	R	=	Reel (3000pcs/reel)

### 10.3 IQS512 Ordering



IC NAME	IQS512	=	IQS512
CONFIGURATION CODE	0000	=	Unprogrammed
PACKAGE TYPE	TS	=	TSSOP20
BULK PACKAGING	R	=	Reel (2500pcs/reel)





## **Changes:**

#### Release v1.00

IQS550 datasheet released

#### Release v1.01

- Removed all communication timeout information (RDY timeout)
- Add normal mode sample filtering and its relative settings
- Changed naming of Low Power sleep time to just Low-Power timing
- Added SleepTime (permanent sleep interval during every cycle)
- Removed Main Oscillator settings, system now only 16MHz, with addition of permanent Sleep to decrease current consumption.

#### **Release v1.02** (Changes implemented to reflect firmware release 54 changes)

- Software changes not affecting this document:
  - Fixed a bit error in XY calculations (only observed with new panels with very high sensor gain)
  - o Changed on-chip i2c from software to hardware implementation
  - o Fixed multiple co-ordinates shown with a single touch on Rx0/Tx0 channel
  - Updated to libraries
  - o Added timer updating/compensating for sleep periods.
  - Updated reseed in AutoATI routine
  - Changed XY calculations to only use positive delta's
  - o Updated PM reseed to use the filtered value if filter enabled
  - Separate reseed counters of Normal Mode and Prox Mode implemented, no longer just one global reseed counter. But they use the same setting.
- Changed the terminology 'Click' to 'Snap' (to remove confusion with mouse 'clicks' also implemented on trackpads)
- Added SYNC settings to ProxSettings0
- Support timing up till 5s (Updated Table 5.13)
- Added selectable debounce values (Added Section 3.14, Updated Table 5.1, Added Section 5.6.18)
- Updated Figure 3.2
- Added second set of settings for non-trackpad channels. ATI and threshold options added.
   (Update Section 3.9, Section 4.3, Section 5.6.12, Table 5.9, Section 3.11, Section 3.12)
- Added an automatic update of the Normal Mode channels when the mode is set to Auto. (since it could be possible that system stays in ProxMode for long periods of time, the LTA's of the NormalMode must be kept up to date). Updated Section 3.4.
- Added selectable 'reverse' sensing to ProxMode channel. Updated Section 3.11 and Section 5.6.10.
- Added selectable Events to trigger the EventMode operation. Updated Section 4.1 and Section 5.6.10.
- Updated Section 4.6.
- Added i2c comms timeout, Updated Section 5.6.14.
- Updated Figure 2.1 and Figure 6.1





#### Release v1.03

- Terminology update and minor fixes
- Changed Pretoria office address
- Updated Patent information

#### Release v1.04

- Changes Product and Project Number (2 bytes each). Also Split version number into a Major Revision, and Minor Revision number (1 byte each). Updated Section 5.6.1.
- Reduced NO\_OF\_FINGERS in XYInfoByte from 4 to 3 bits. And added a global Snap status to the available bit. Updated Section 5.6.2.
- Added I2C read rights to all the settings, so that designer can read back settings for confirmation. Updated Table 5.1.
- Added ProxMode layout suggestion to Section 3.3 (added Figure 3.1)

#### Release v1.05

- Updated front page
- Added NRST cap to Figure 6.1
- Added Figure 6.4 application circuit
- Updated ATI Target description in Table 5.21 and Table 5.10

#### Release v1.06

- Included IQS525 and IQS512 throughout datasheet:
  - Modified front page
  - o Added Sections 2.2, 2.3, 8.3, 8.4, 8.5, 8.6, 9.2, 9.3, 10.2, 10.3
  - o Update Table 5.14, Table 5.16, Table 7.6
  - Added circuit diagrams to Section 6
- Changed report rate Section 3.6 and added Table 7.7
- Changed pin name Vdd to Vddhi
- Updated Ordering information
- Add SingleXY output mode, update Section 5.6.10
- Add new proxmode option, summary channel (all trackpad channels together). Updated Section 5.6.15





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