

EN: This Datasheet is presented by the manufacturer.

Please visit our website for pricing and availability at www.hestore.hu.

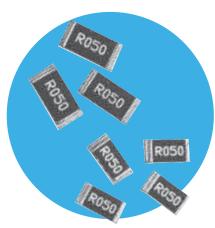
## **Resistors**

# Electronics Make Possible

## **Low Value Flat Chip Resistor**

#### **LR Series**

- Standard 2512, 2010 and 1206 sizes
- Resistance values down to 0.003 ohms
- Leach resistant solder-plated copper wrap-around termination
- AEC-Q200 Qualified
- RoHS compliant and SnPb variants





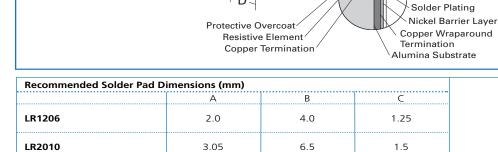
### **Electrical Data**

		LR1206	LR2010	LR2512			
Power rating @70°C	watts	0.5	2				
Resistance range <sup>1</sup>	ohms	R003 to 1R0					
Resistance tolerance <sup>1</sup>	%	<r01:5, td="" ≥r01:1,2,5<=""></r01:5,>					
TCR	ppm/°C	≥R05: ±100, R025-R047: <+200, R015-R024: <+300, R01-R014: <+500, <r01: <+900<="" td=""></r01:>					
Dielectric withstand	volts	200					
Ambient temperature range	°C	-55 to +150					
Values		E24 preferred <sup>2</sup>					
Temperature rise at rated power	°C	40	80	90			
Pad / trace area <sup>3</sup>	mm²	30	100	300			

Note 1: Contact factory for value – tolerance combinations outside this range. Note 2: Many values =  $N \times R001$  and  $N \times R005$  up to N=10 are also available. Note 3: Recommended minimum pad & adjacent trace area for each termination for rated dissipation on FR4 PCB

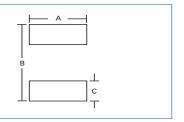
## Physical Data

Dimensions (mm)				
Size	L	W	H (max)	D
LR1206	3.20±0.305	1.63±0.20	0.8	0.48±0.25
LR2010	5.23±0.38	2.64±0.25	0.84	0.48±0.25
LR2512	6.50±0.38	3.25±0.25	0.84	0.48±0.25
	W	LR 1206 / 2	010 / 2512	0.1020.2



7.75

1.5



#### General Note

LR2512

TT Electronics reserves the right to make changes in product specification without notice or liability. All information is subject to TT Electronics' own data and is considered accurate at time of going to print.

3.7



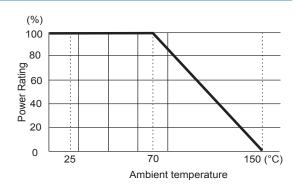
www.ttelectronicsresistors.com

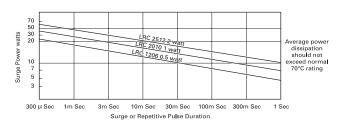
## **Low Value Flat Chip Resistor**





ref	AEC-Q200 Table 7 Test	Method	Ma (add I	<b>Typ.</b> (@1R0)	
3	High Temp. Exposure	MIL-STD-202 Method 108	ΔR%	0.5	0.2
4	Temperature Cycling	JESD22 Method JA-104	∆R%	0.25	0.1
6	Moisture Resistance	MIL-STD-202 Method 106	ΔR%	0.5	0.2
7	Biased Humidity	MIL-STD-202 Method 103	ΔR%	0.5	0.2
8	Operational Life (Cyclic Load)	MIL-STD-202 Method 108	ΔR%	1	0.5
14	Vibration	MIL-STD-202 Method 204	ΔR%	0.5	0.05
15	Resistance to Soldering Heat	MIL-STD-202 Method 210	∆R%	0.25	0.05
16	Thermal Shock	MIL-STD-202 Method 107	ΔR%	0.25	0.1
18	Solderability	J-STD-002	>95%	erage	
21	Board Flex	AEC-Q200-005	ΔR%	0.5	0.2
22	Terminal Strength	AEC-Q200-006	ΔR%	0.25	0.1
Short Term Overload		6.25 x Pr for 2s	ΔR%	0.5	
	Low Temperature Storage	-65°C for 100 hours	<b>Δ</b> R% 0.5		
	Leach Resistance	Solder dip at 250°C	90s minimu		num





#### Note:

- 1. Although 2010 and 2512 sizes have passed temperature cycling and thermal shock, it is in general not recommended that ceramic chips this large be used on FR4 in a severe temperature cycle environment due to the possibility of solder joint fatigue.
- 2. Full AEC-Q200 qualification applies only to European Part Numbers at ohmic values ≥R01.

## **Ordering Procedure**

This product has two valid part numbers:

European (Welwyn) Part Number: LRF1206-R02FW (1206, 20 milliohms ±1%, Pb-free)



1	2	3	4	5			
Туре	Size	Value	Tolerance	Termination & Packing			
LR = Conventional orientation	1206 E24 = 3/4 F = ±1% W Pb-free, standard				ree, standard packing		
(values >R025)	2010 characters		G = ±2%	T1	Pb-free, 1000/reel (non-standard		
LRF = Flip-chip orientation	2512	R = ohms	J = ±5%	PB SnPb finish, stand		inish, standard packing	
(values ≤R025)				;	Standard	packing is tape & reel	
·				120	6 & 2010	3000/reel	
					2512	1800/reel	

USA (IRC) Part Number: LRC-LRF1206LF-01-R020-F (1206, 20 milliohms ±1%, Pb-free)



1 Fam	2 ily Model	3 Size	4 Termination	5 TCR	6 Value	7 Tolerance	Packi	ng
LR	LR = Conventional orientation (values >R025)	1206 2010	Omit for SnPb LF = Pb-free	01 = standard (±100ppm/°C		F = ±1% G = ±2%	Standard packing is tape & reel	
	LRF = Flip-chip orientation (values ≤R025)	2512		values ≥R05)		J = ±5%	1206 & 2010 2512	3000/reel 1800/reel

#### General Note